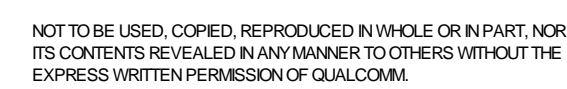


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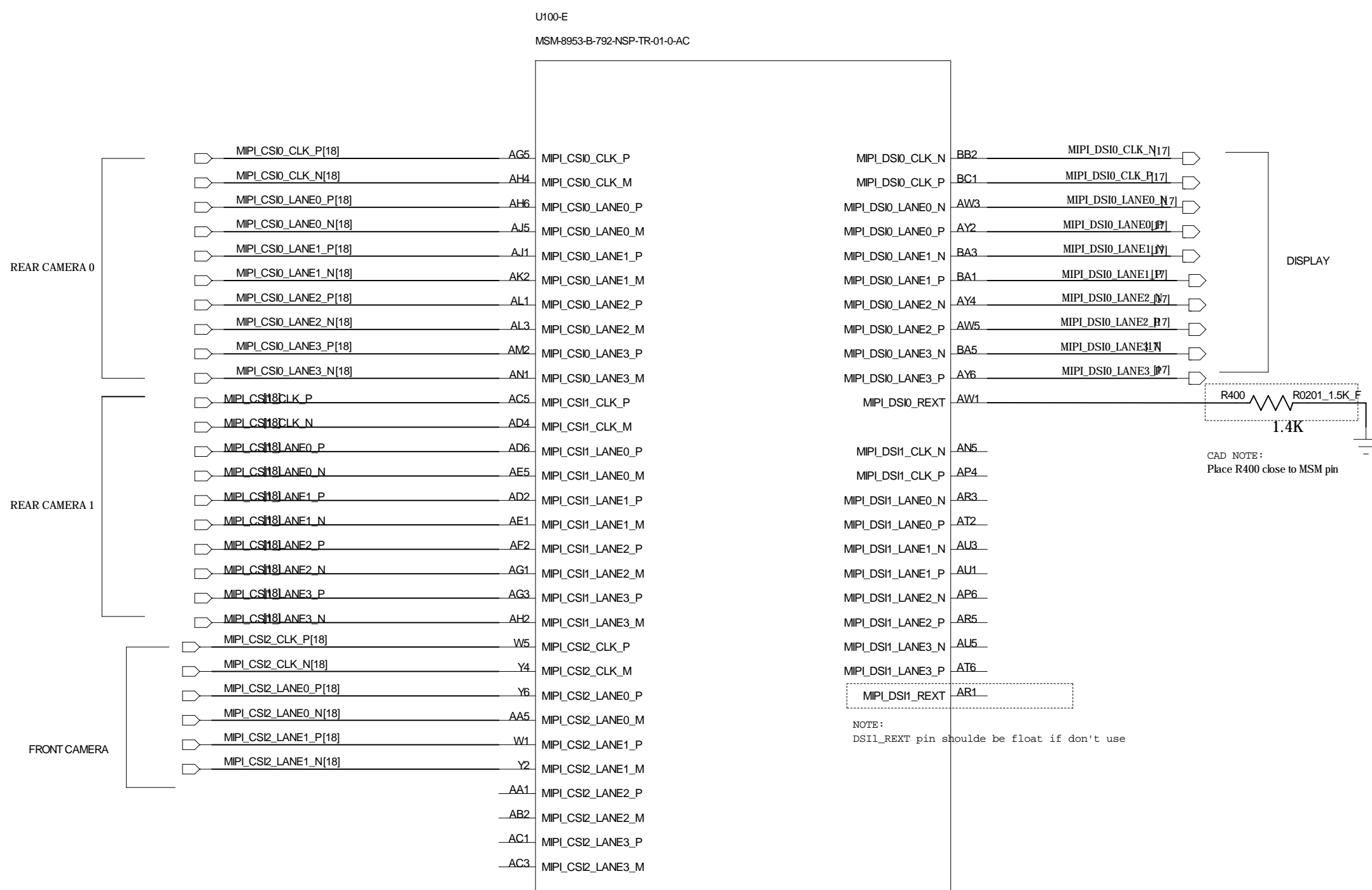
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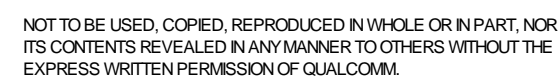


CSI Pin Name	CSI DPHY 4-lane	CSI DPHY 2+1 Mode	CSI CPHY 3Phase Mode
CSIO_3PHASE_PIN0	CSIO_CLKP	CSIO_2LANE_CLKP	NC
CSIO_3PHASE_PIN1	CSIO_CLKN	CSIO_2LANE_CLKN	CSIO_TRI0_A
CSIO_3PHASE_PIN2	CSIO_DP0	CSIO_2LANE_DP0	CSIO_TRI0_B
CSIO_3PHASE_PIN3	CSIO_DN0	CSIO_2LANE_DN0	CSIO_TRI0_C
CSIO_3PHASE_PIN4	CSIO_DP1	CSIO_2LANE_DP1	CSIO_TRI1_A
CSIO_3PHASE_PIN5	CSIO_DN1	CSIO_2LANE_DN1	CSIO_TRI1_B
CSIO_3PHASE_PIN6	CSIO_DP2	CSIO_1LANE_DP0	CSIO_TRI1_C
CSIO_3PHASE_PIN7	CSIO_DN2	CSIO_1LANE_DN0	CSIO_TRI2_A
CSIO_3PHASE_PIN8	CSIO_DP3	CSIO_1LANE_CLKP	CSIO_TRI2_B
CSIO_3PHASE_PIN9	CSIO_DN3	CSIO_1LANE_CLKN	CSIO_TRI2_C
CSII_3PHASE_PIN1	CSII_CLKN	CSII_2LANE_CLKN	CSII_TRI0_A
CSII_3PHASE_PIN2	CSII_DP0	CSII_2LANE_DP0	CSII_TRI0_B
CSII_3PHASE_PIN3	CSII_DN0	CSII_2LANE_DN0	CSII_TRI0_C
CSII_3PHASE_PIN4	CSII_DP1	CSII_2LANE_DP1	CSII_TRI1_A
CSII_3PHASE_PIN5	CSII_DN1	CSII_2LANE_DN1	CSII_TRI1_B
CSII_3PHASE_PIN6	CSII_DP2	CSII_1LANE_DP0	CSII_TRI1_C
CSII_3PHASE_PIN7	CSII_DN2	CSII_1LANE_DN0	CSII_TRI2_A
CSII_3PHASE_PIN8	CSII_DP3	CSII_1LANE_CLKP	CSII_TRI2_B
CSII_3PHASE_PIN9	CSII_DN3	CSII_1LANE_CLKN	CSII_TRI2_C
CSI2_3PHASE_PIN1	CSI2_CLKN	CSI2_2LANE_CLKN	CSI2_TRI0_A
CSI2_3PHASE_PIN2	CSI2_DP0	CSI2_2LANE_DP0	CSI2_TRI0_B
CSI2_3PHASE_PIN3	CSI2_DN0	CSI2_2LANE_DN0	CSI2_TRI0_C
CSI2_3PHASE_PIN4	CSI2_DP1	CSI2_2LANE_DP1	CSI2_TRI1_A
CSI2_3PHASE_PIN5	CSI2_DN1	CSI2_2LANE_DN1	CSI2_TRI1_B
CSI2_3PHASE_PIN6	CSI2_DP2	CSI2_1LANE_DP0	CSI2_TRI1_C
CSI2_3PHASE_PIN7	CSI2_DN2	CSI2_1LANE_DN0	CSI2_TRI2_A
CSI2_3PHASE_PIN8	CSI2_DP3	CSI2_1LANE_CLKP	CSI2_TRI2_B
CSI2_3PHASE_PIN9	CSI2_DN3	CSI2_1LANE_CLKN	CSI2_TRI2_C

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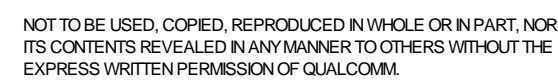
MSM8953 MIPI

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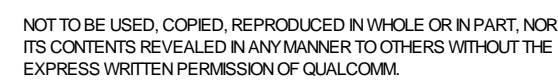
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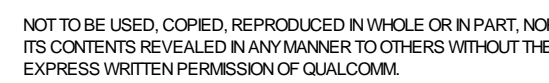
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Title

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Size	Name

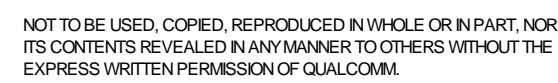
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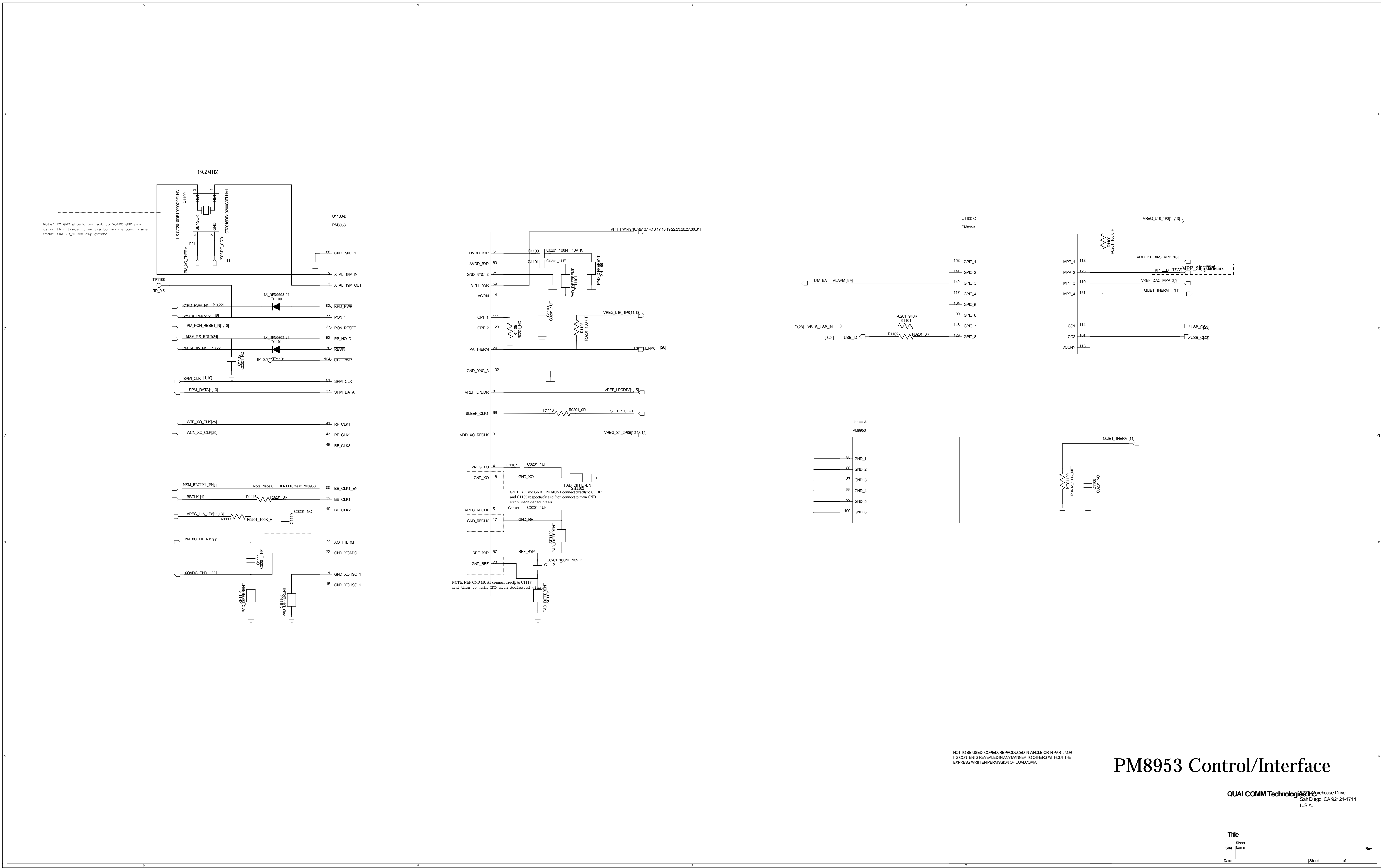
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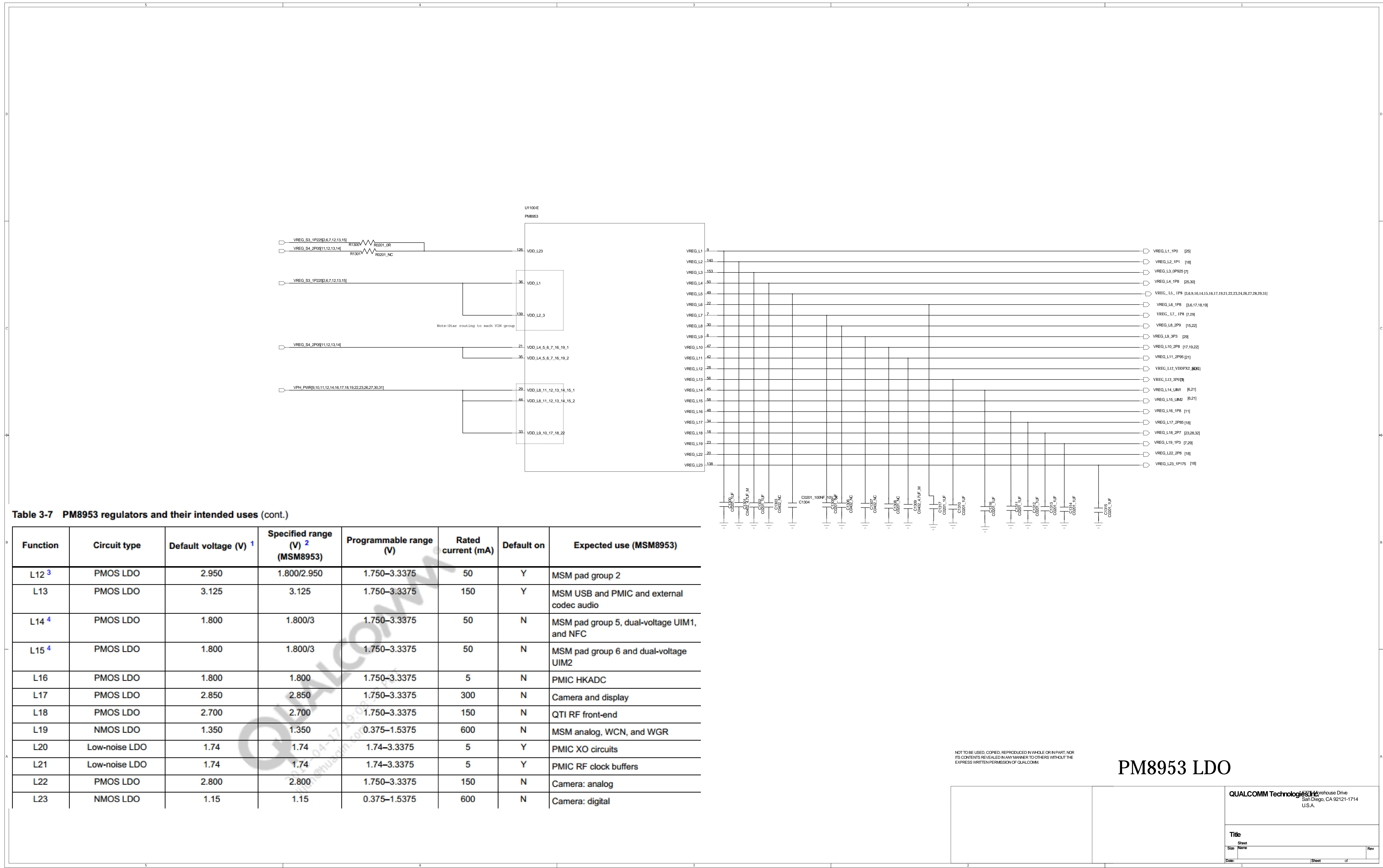
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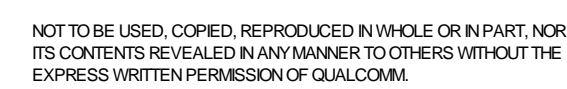


PM8953 Control/Interface

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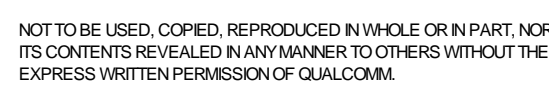
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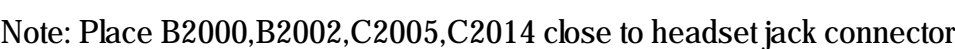
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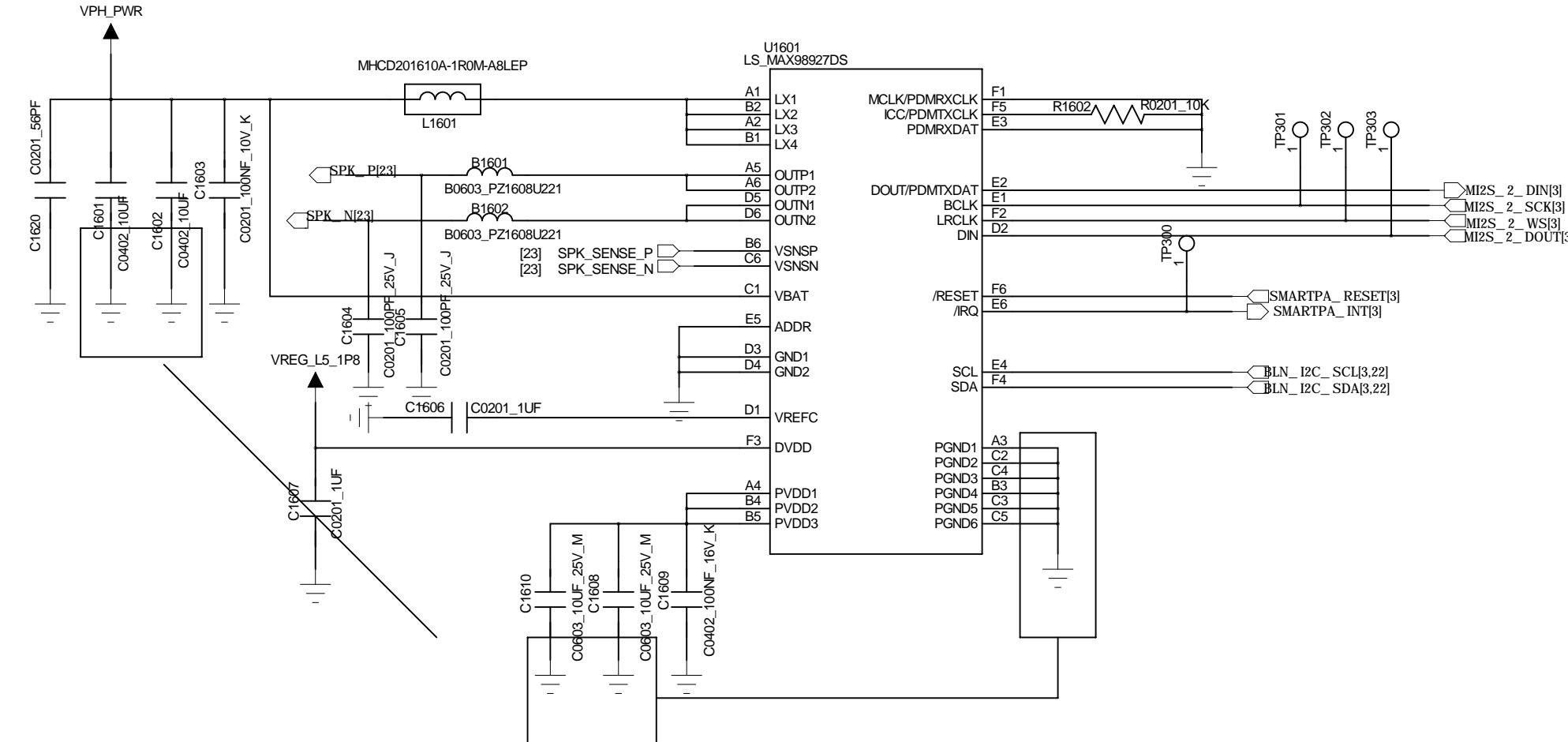


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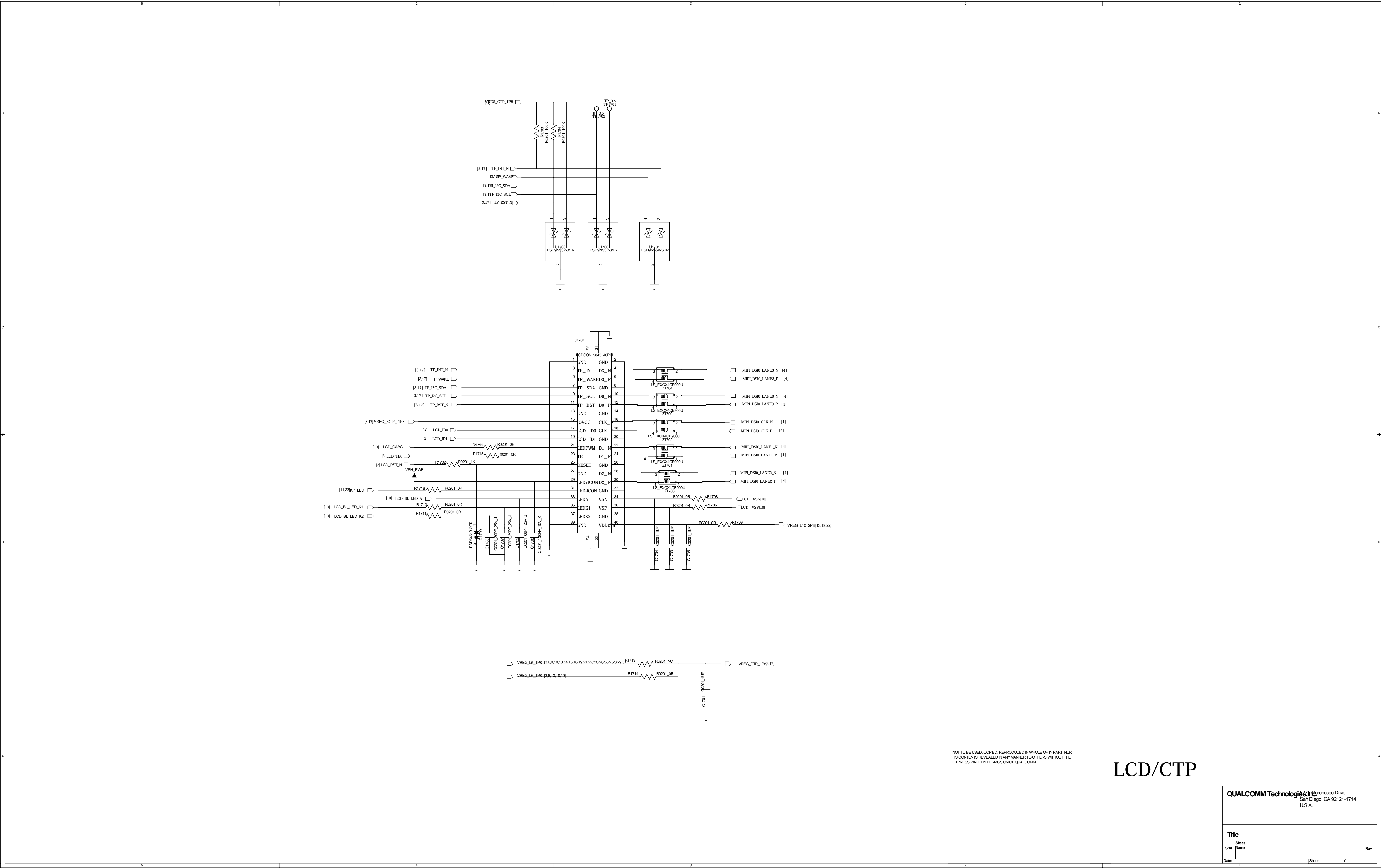
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Smart PA



WCD9326 CODEC



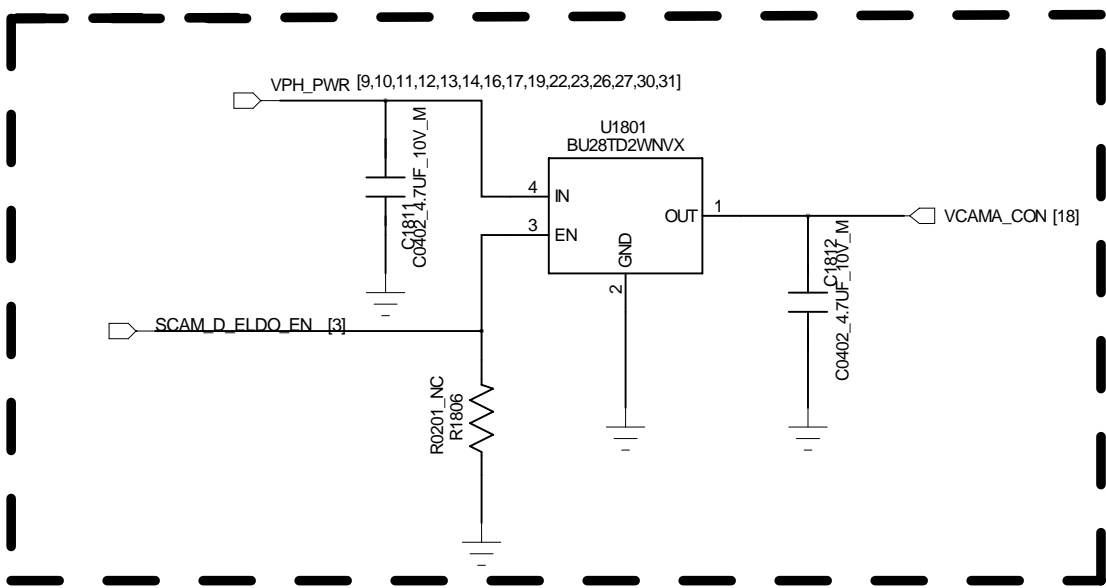
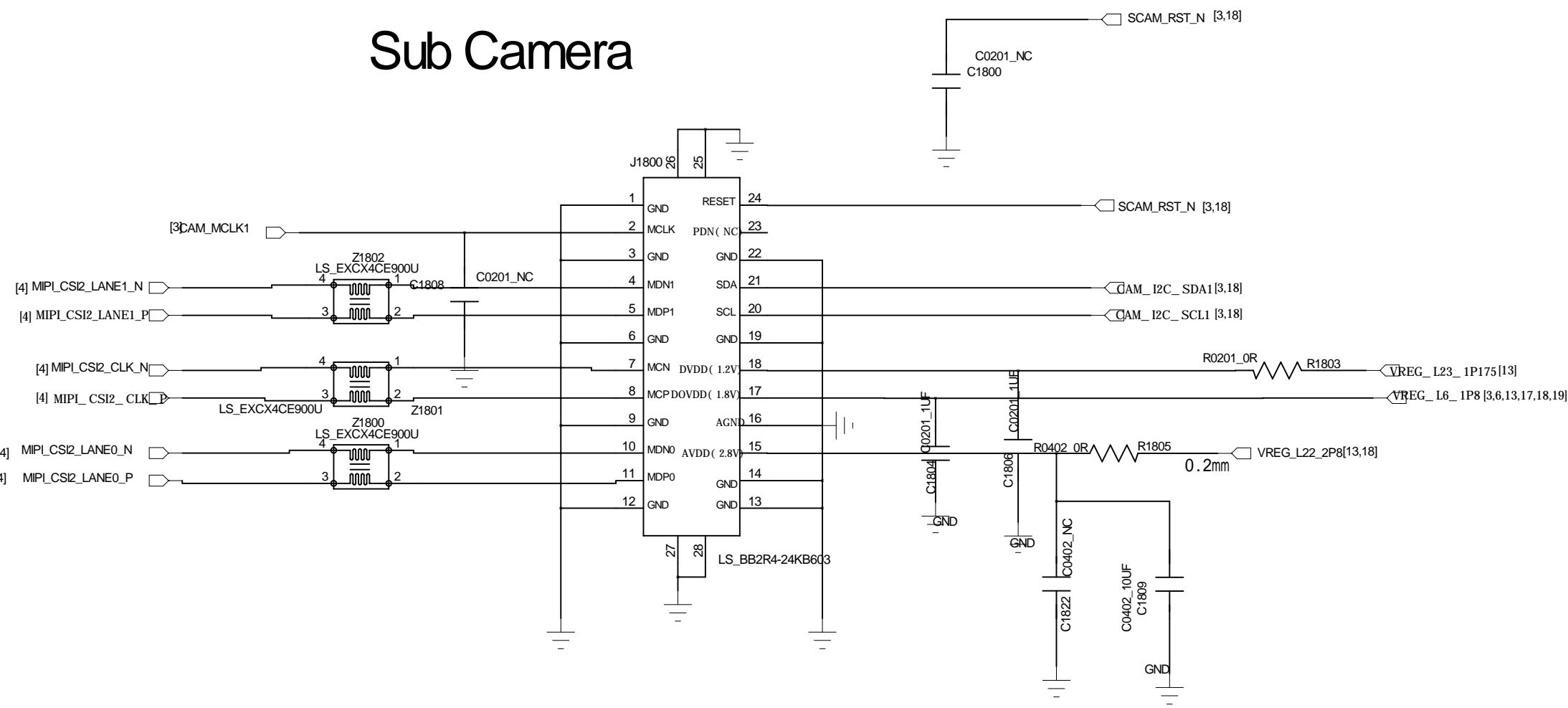
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LCD/CTP

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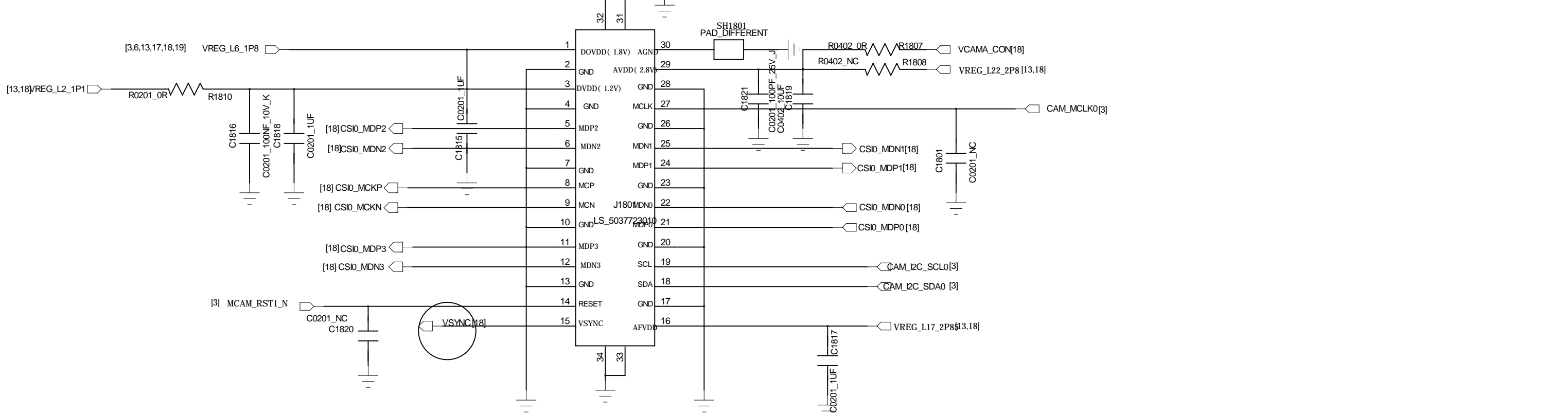
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Sub Camera

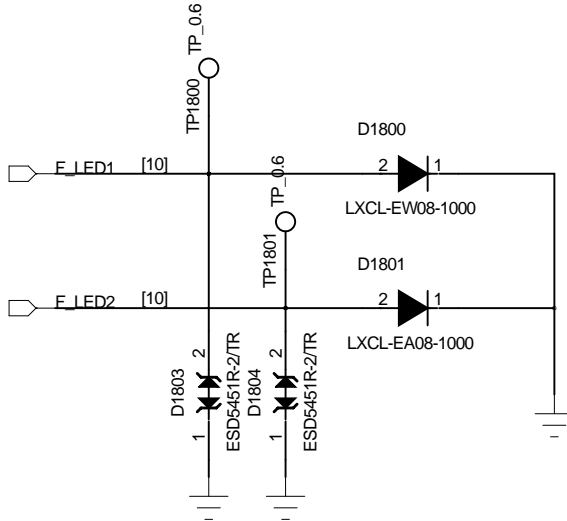
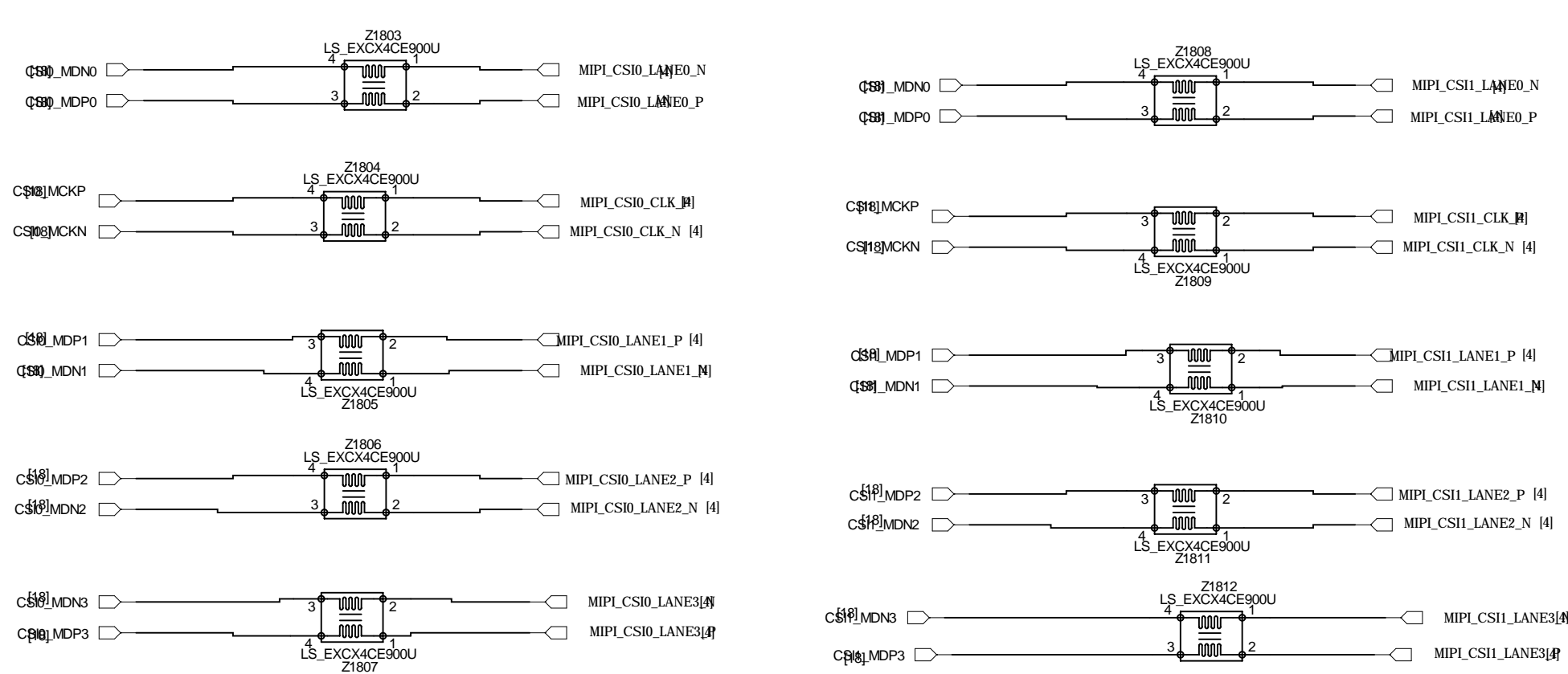
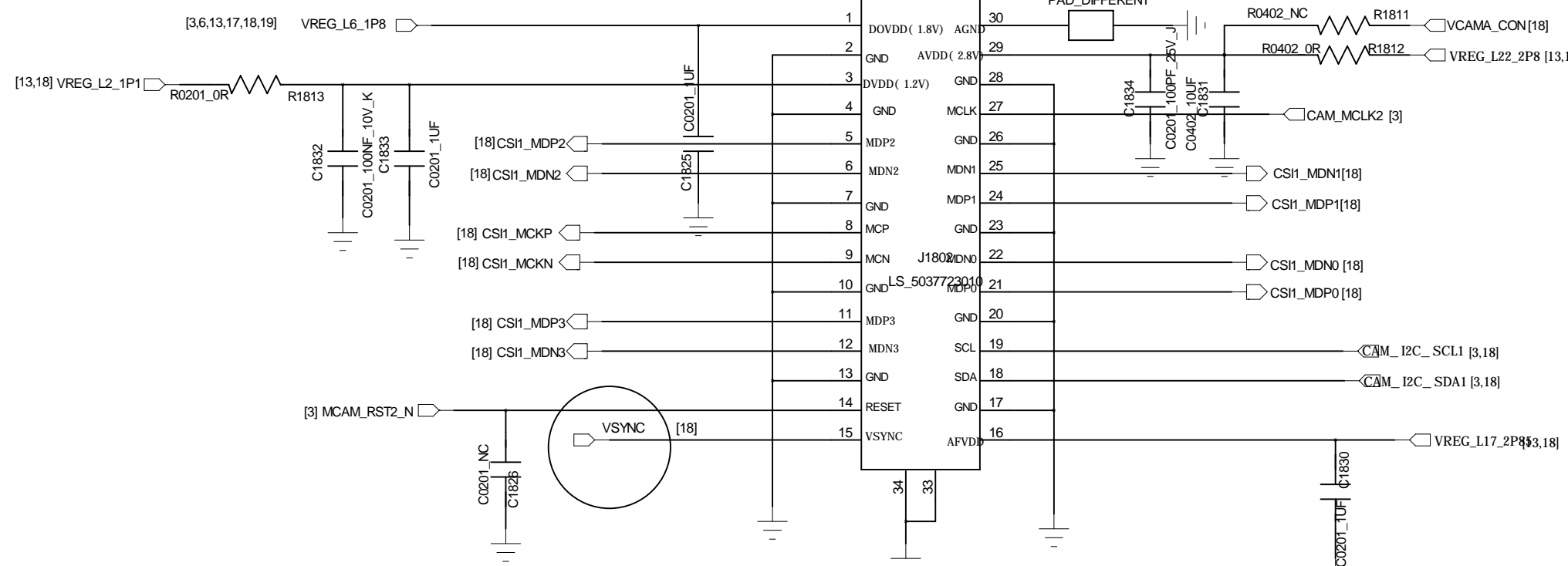


Main CAMERA

Master



Slave



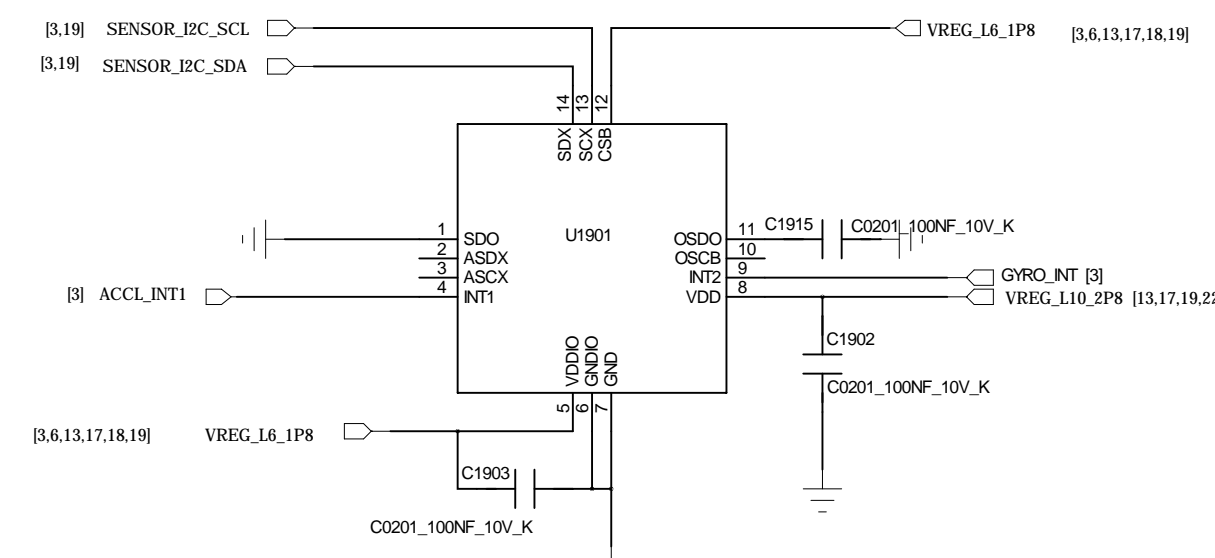
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Camera/Flash

VANA and VDIG and VIF may rise in any order
Sensor I2C addr:x020
Driver IC I2C addr:0x84

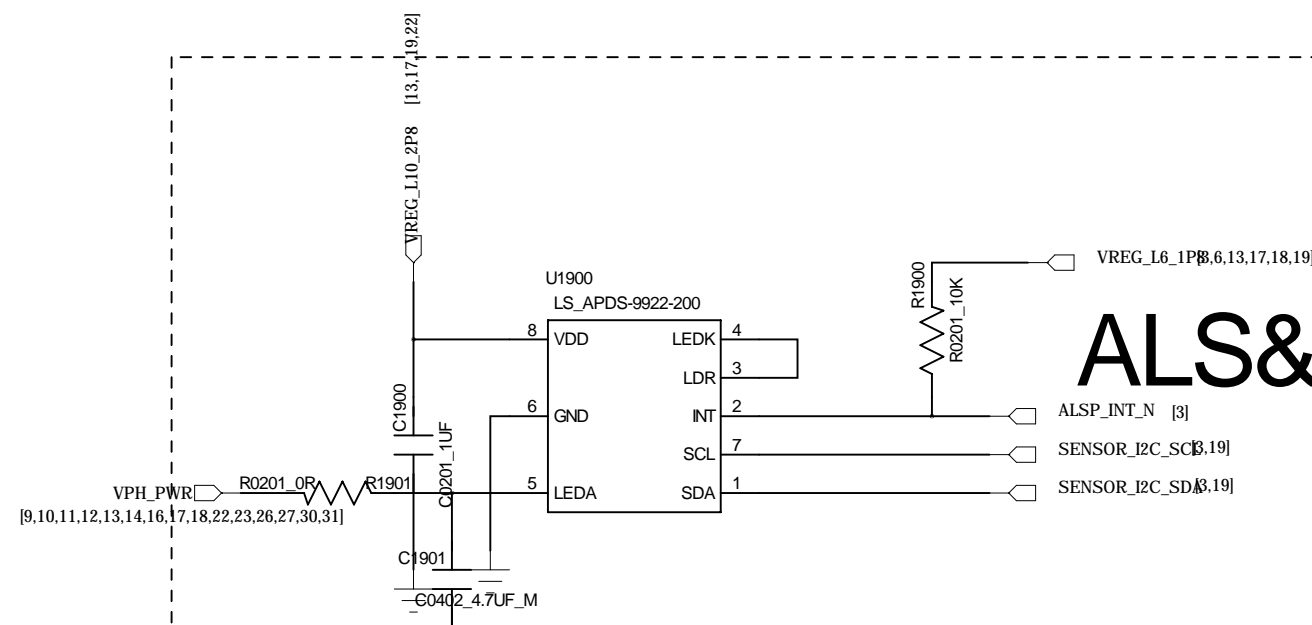
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Date		

Gyro+G-sensor

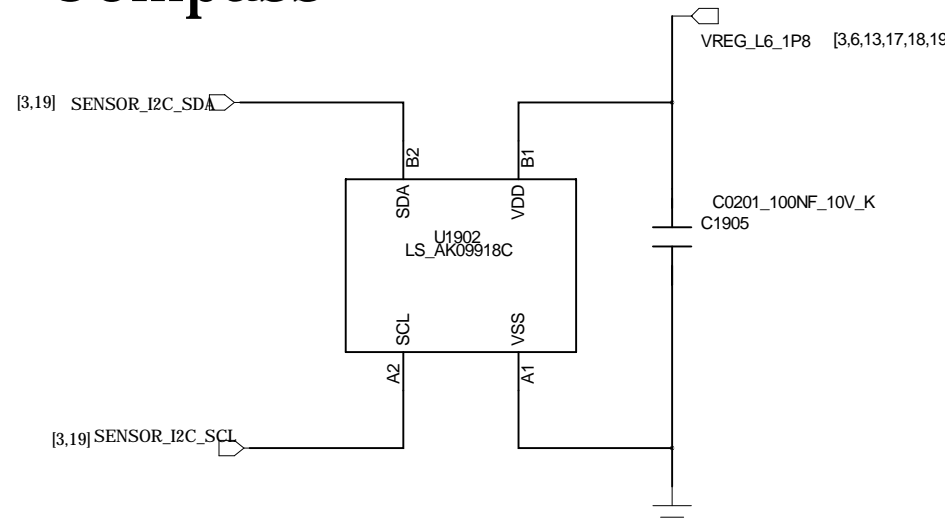


0x60H 7bit

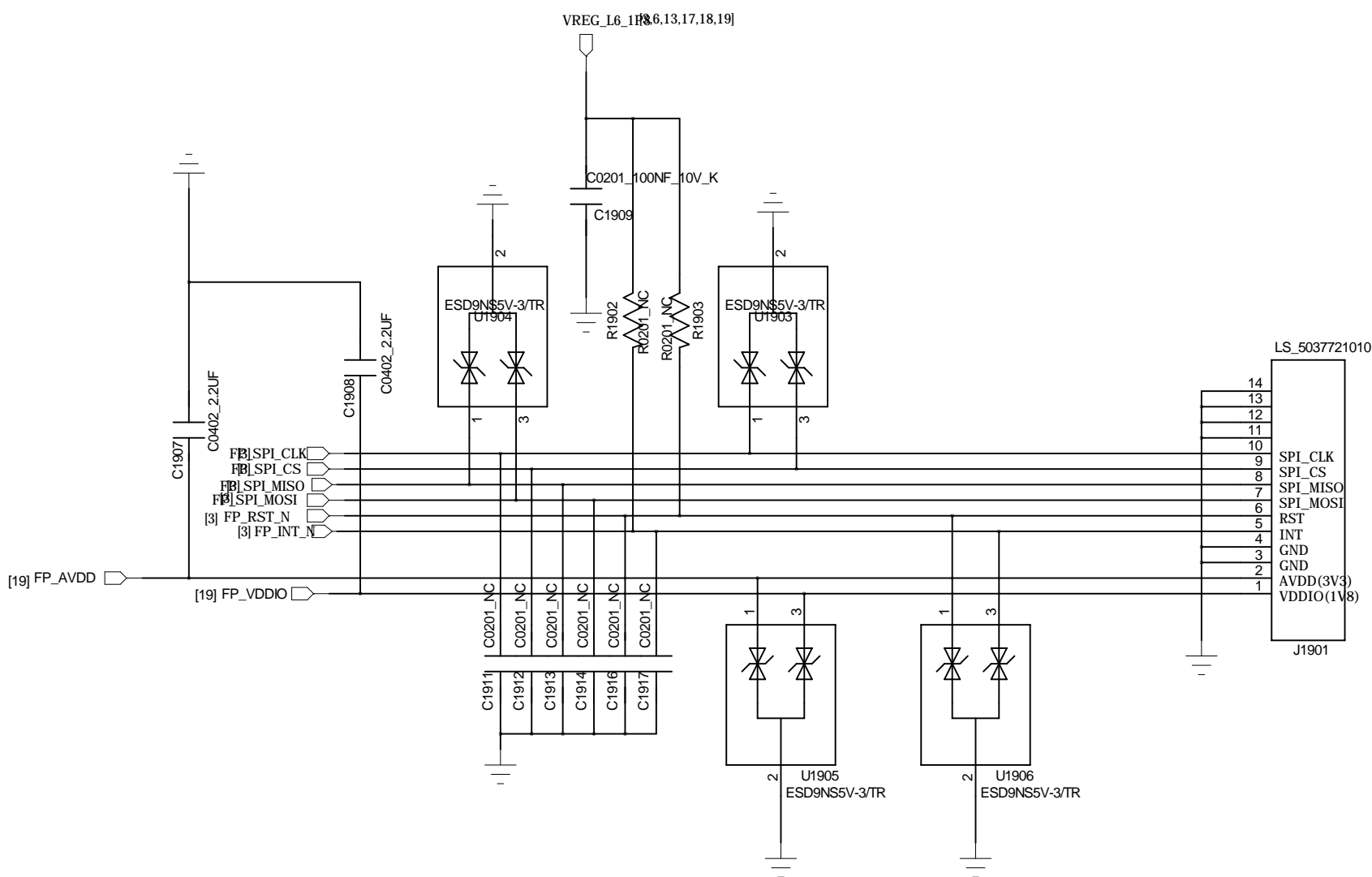
ALS&PS



Compass



[13,17,16,2] VREG_L10_2P9 R200 R200_0R TP_AVDD 114
VREG_L1_114 R120 R120_0R TP_VDDIO15
[14,5,10,11,12,13,14,15,16,17,17,23,24,28,27,28,25,1]



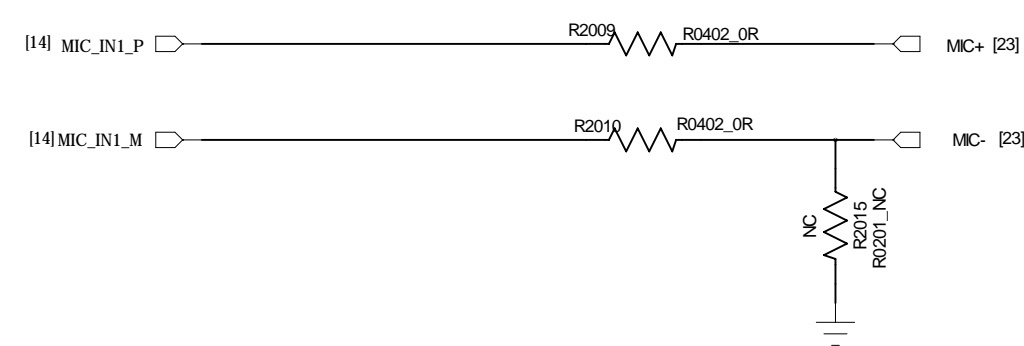
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Sensor

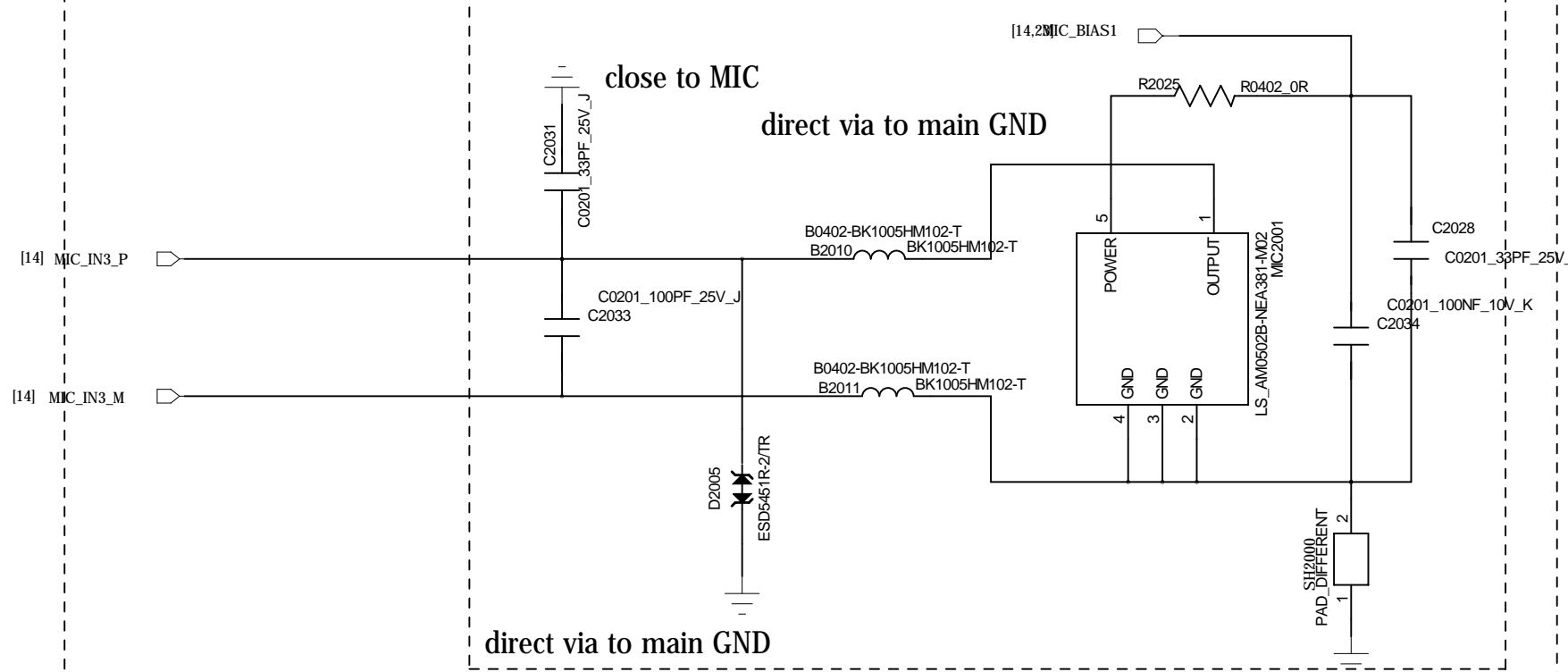
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Date	Sheet	of

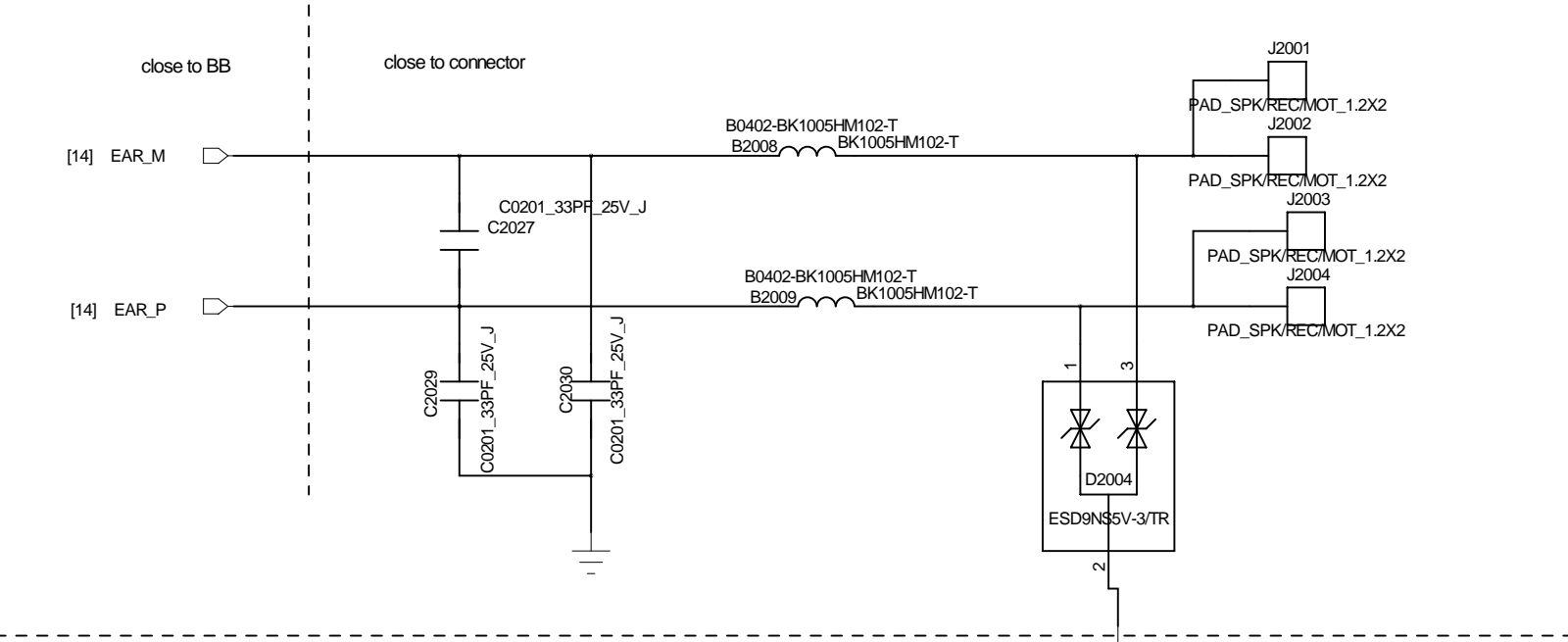
MIC -- Main



MIC -- Sub



RECEIVER



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Audio Interface

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Title		
Doc	Rev	Rev
1	1	1



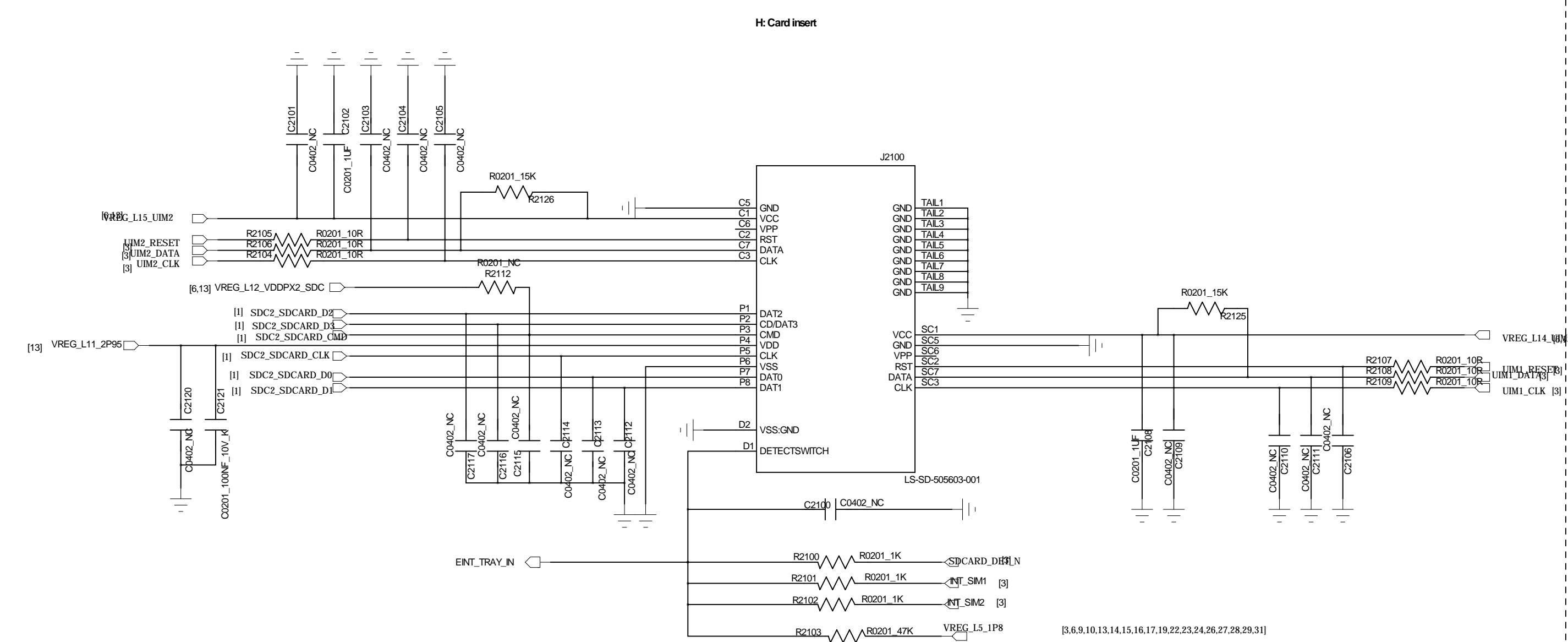
SM1→Inside

SMT2→Outside

SW2CD

L: Card remove
H: Card insert

H: Card insert



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SIM/TF card

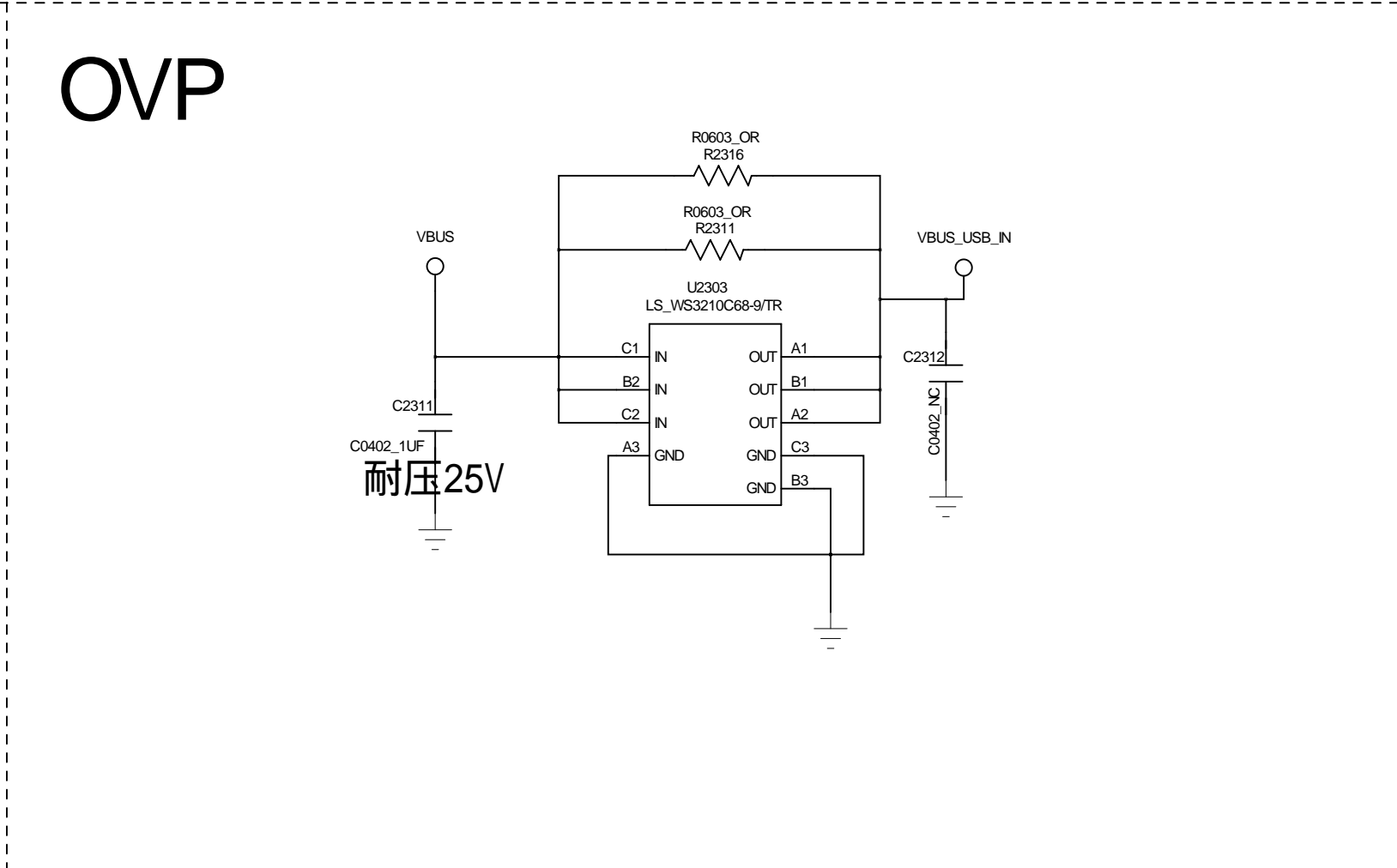
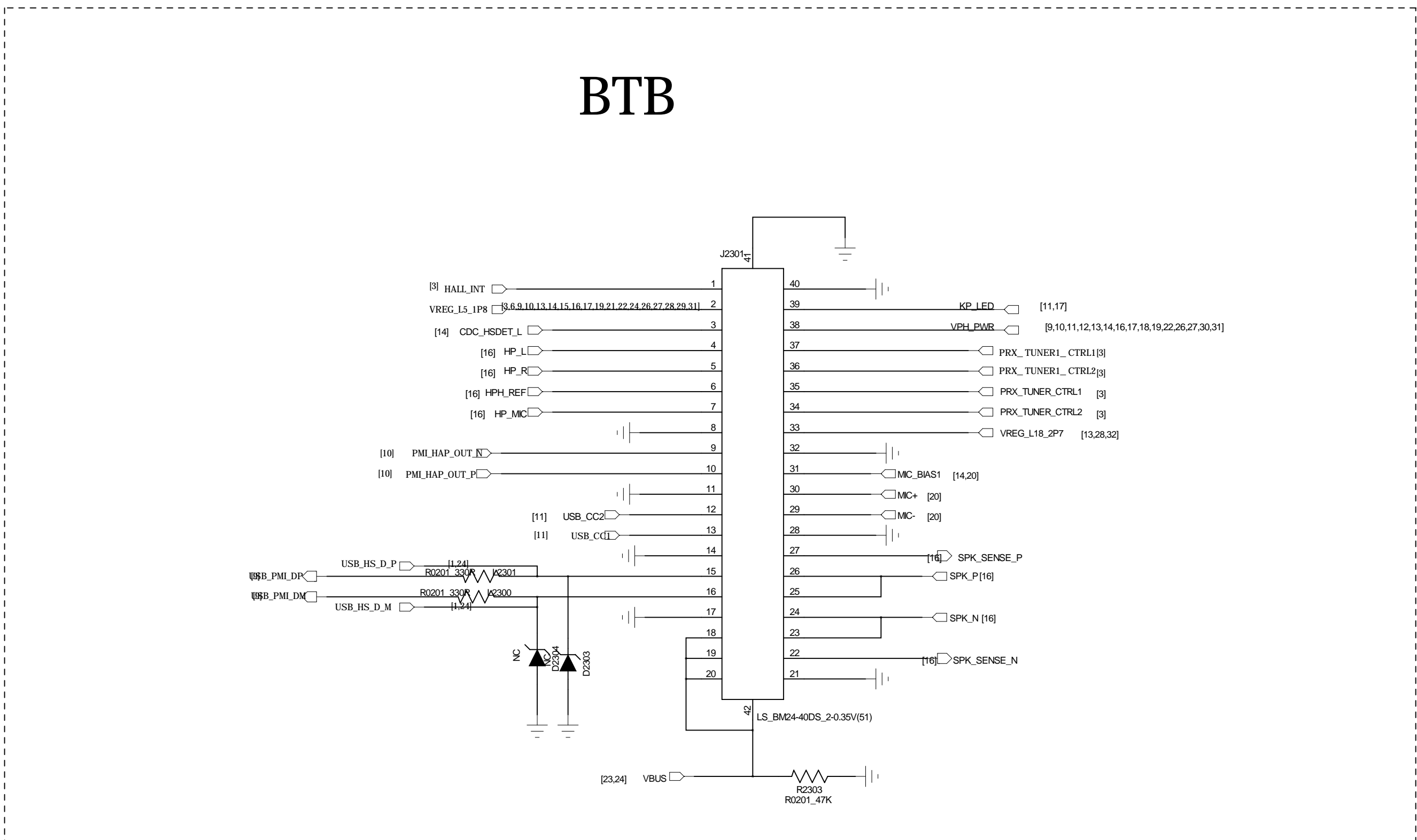
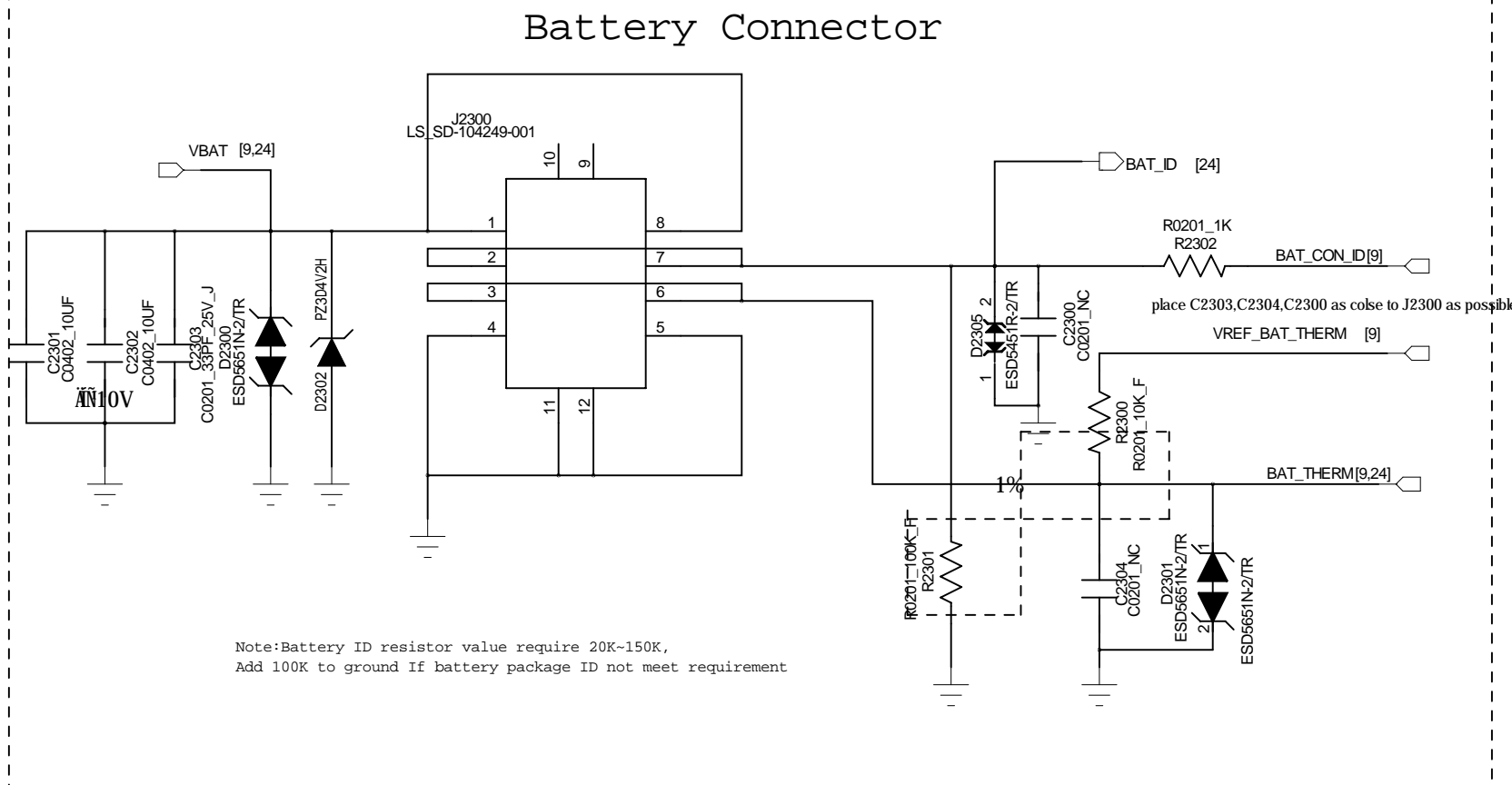
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Title	Author	Date	Page
1. Introduction	John Doe	2023-10-27	10
2. Methodology	Jane Smith	2023-10-28	15
3. Results	Mike Johnson	2023-10-29	20
4. Discussion	Emily White	2023-10-30	25
5. Conclusion	David Brown	2023-10-31	30

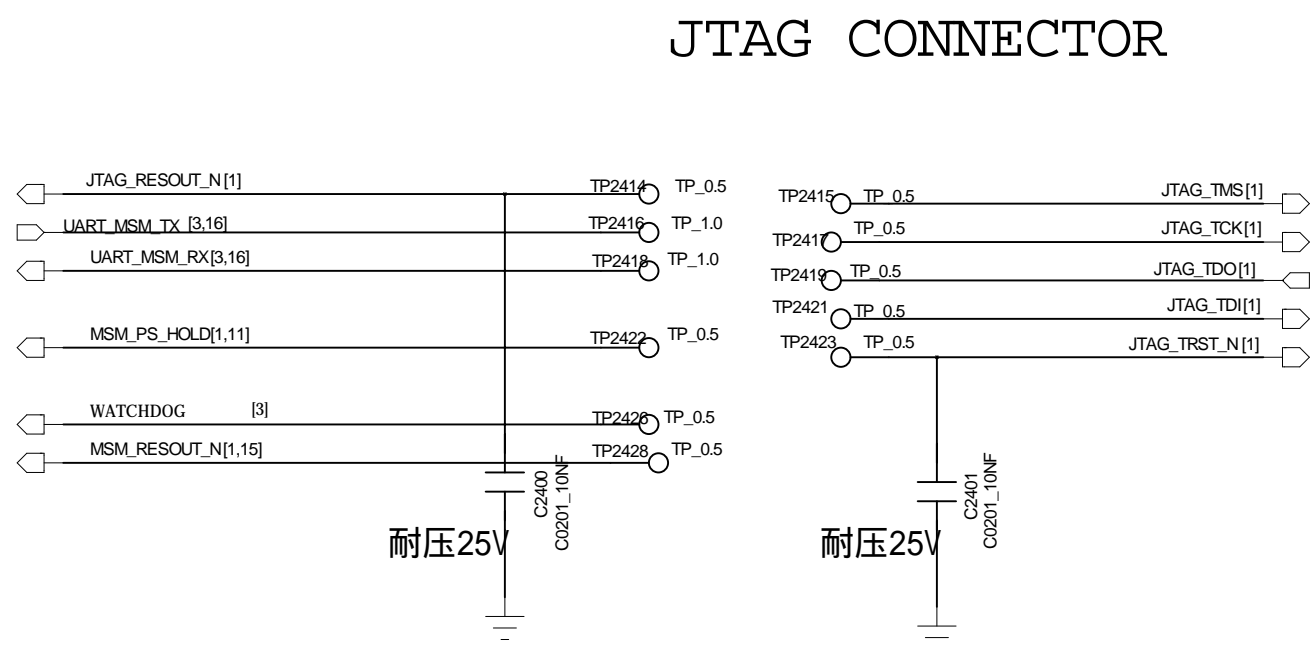
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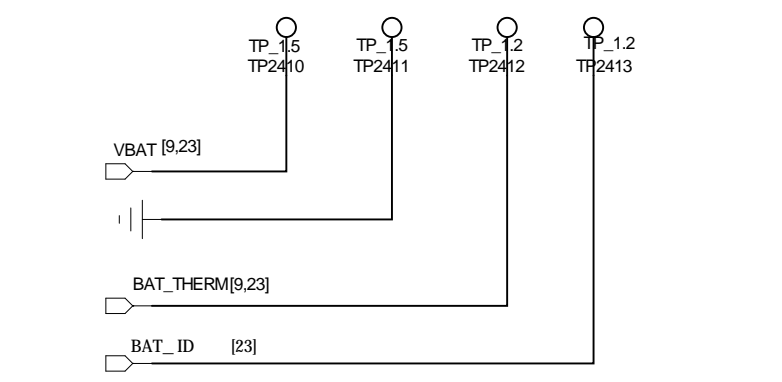
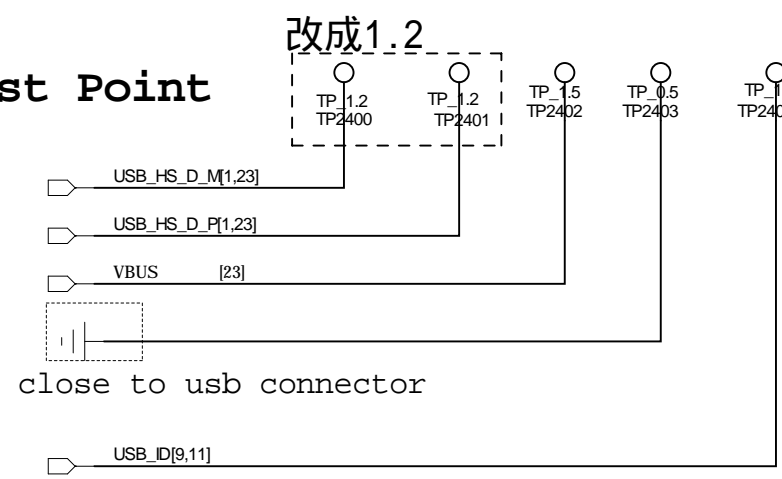
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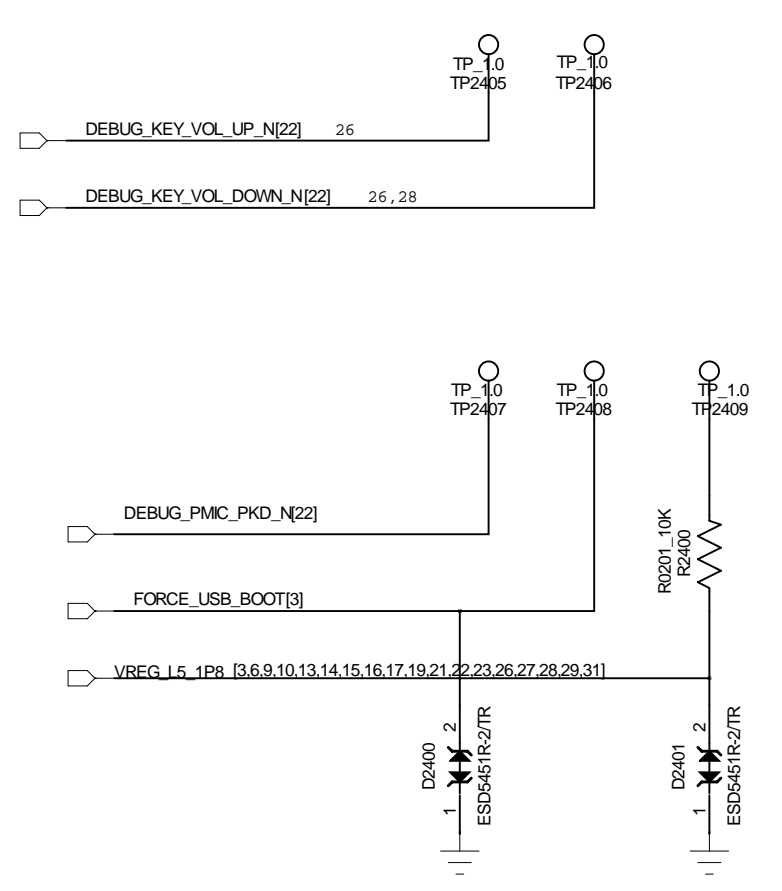
Connector(USB/Sub- board)



Test Point



PCBA AUTOMATIC TEST point



F81
F82
F83
F84

F81
F82
F83
F84

F81
F82
F83
F84

F81
F82
F83
F84

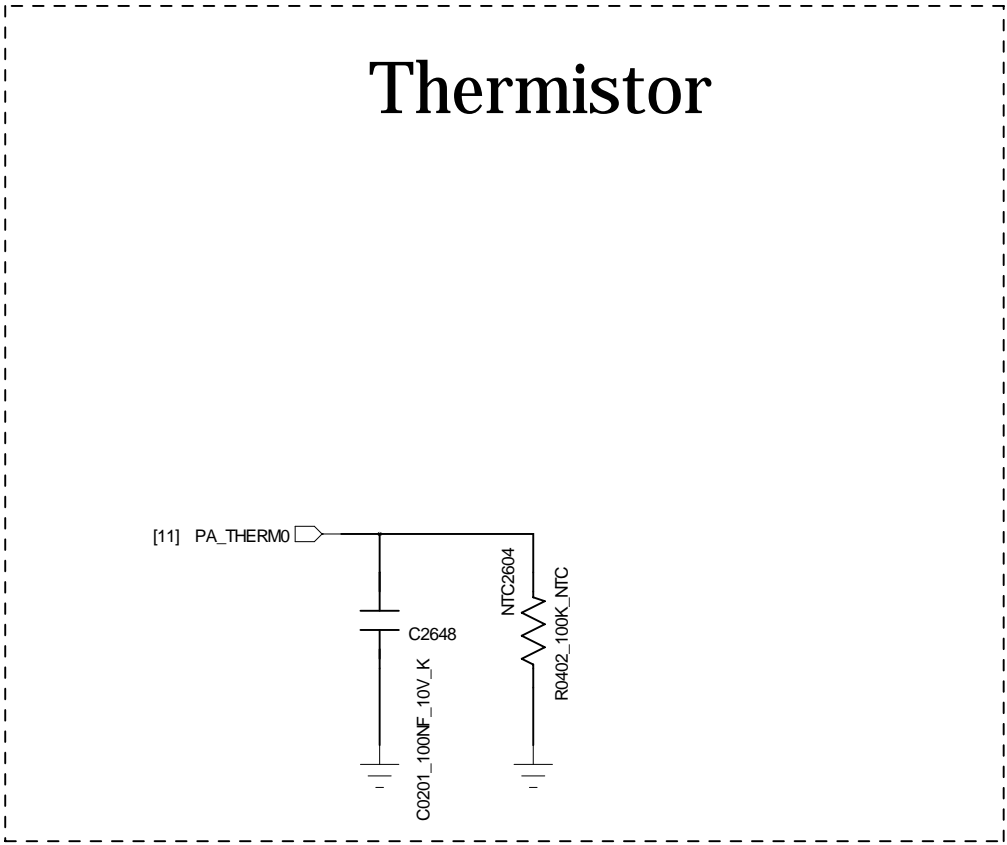
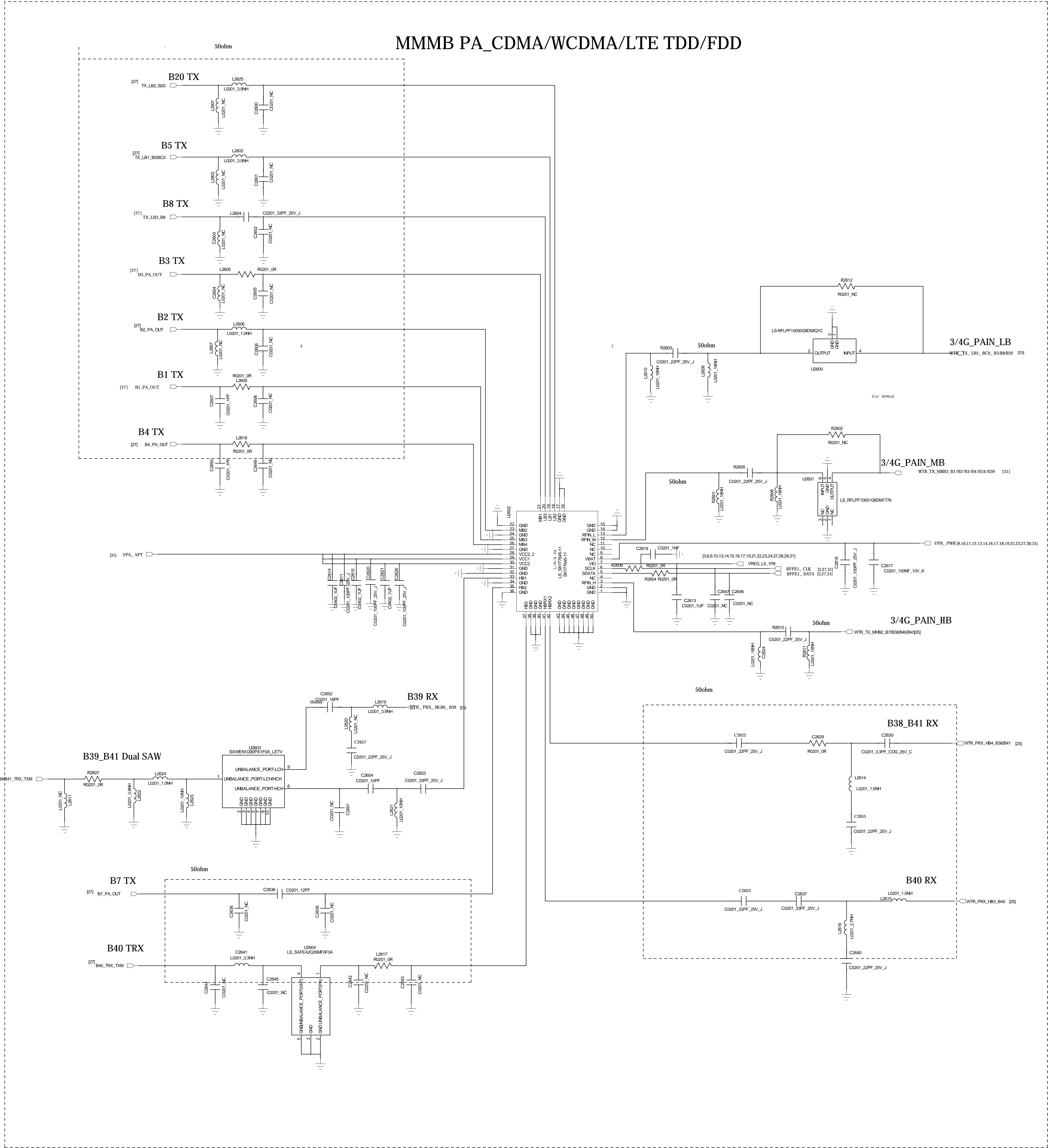
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TP/GND/Shielding

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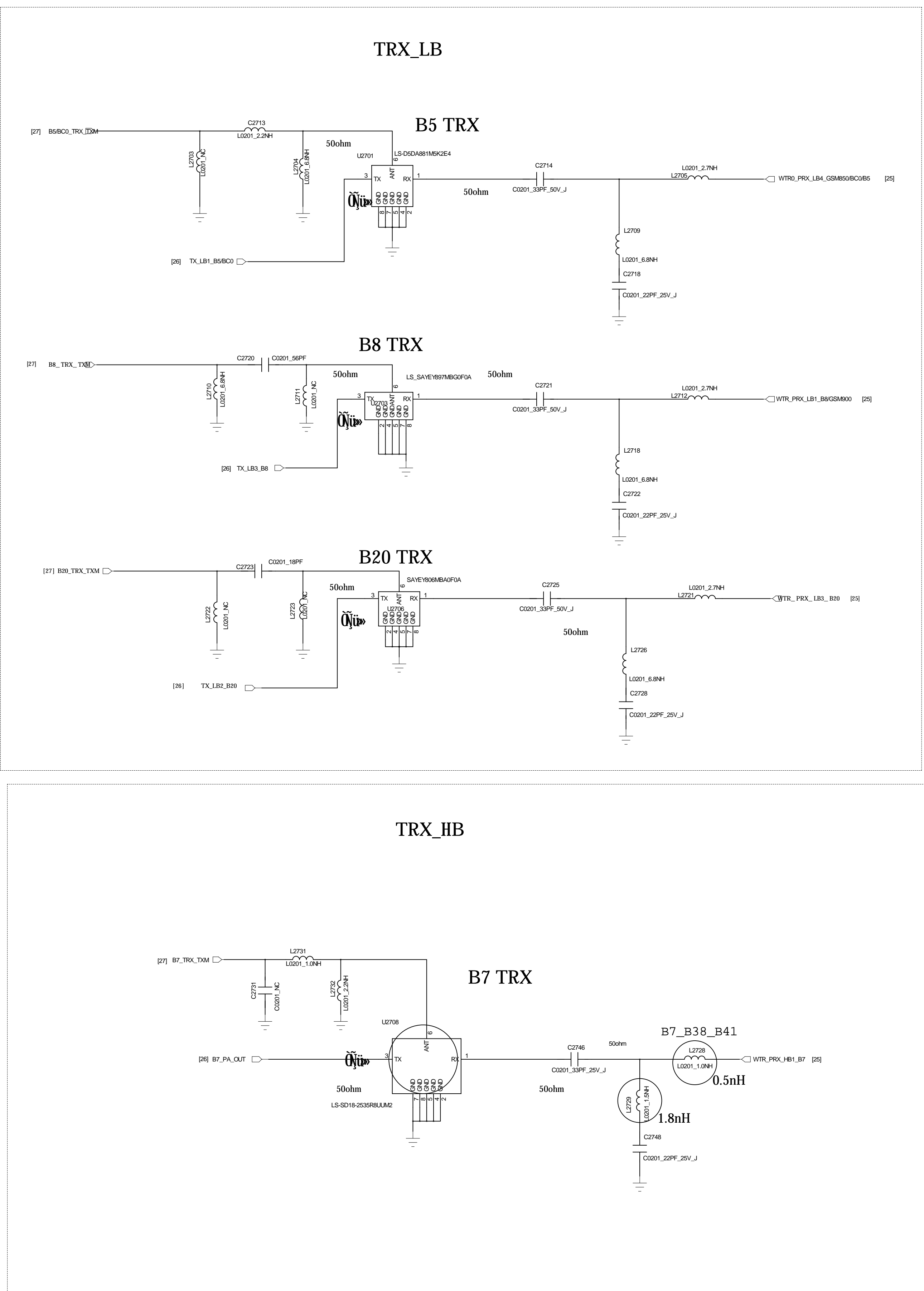
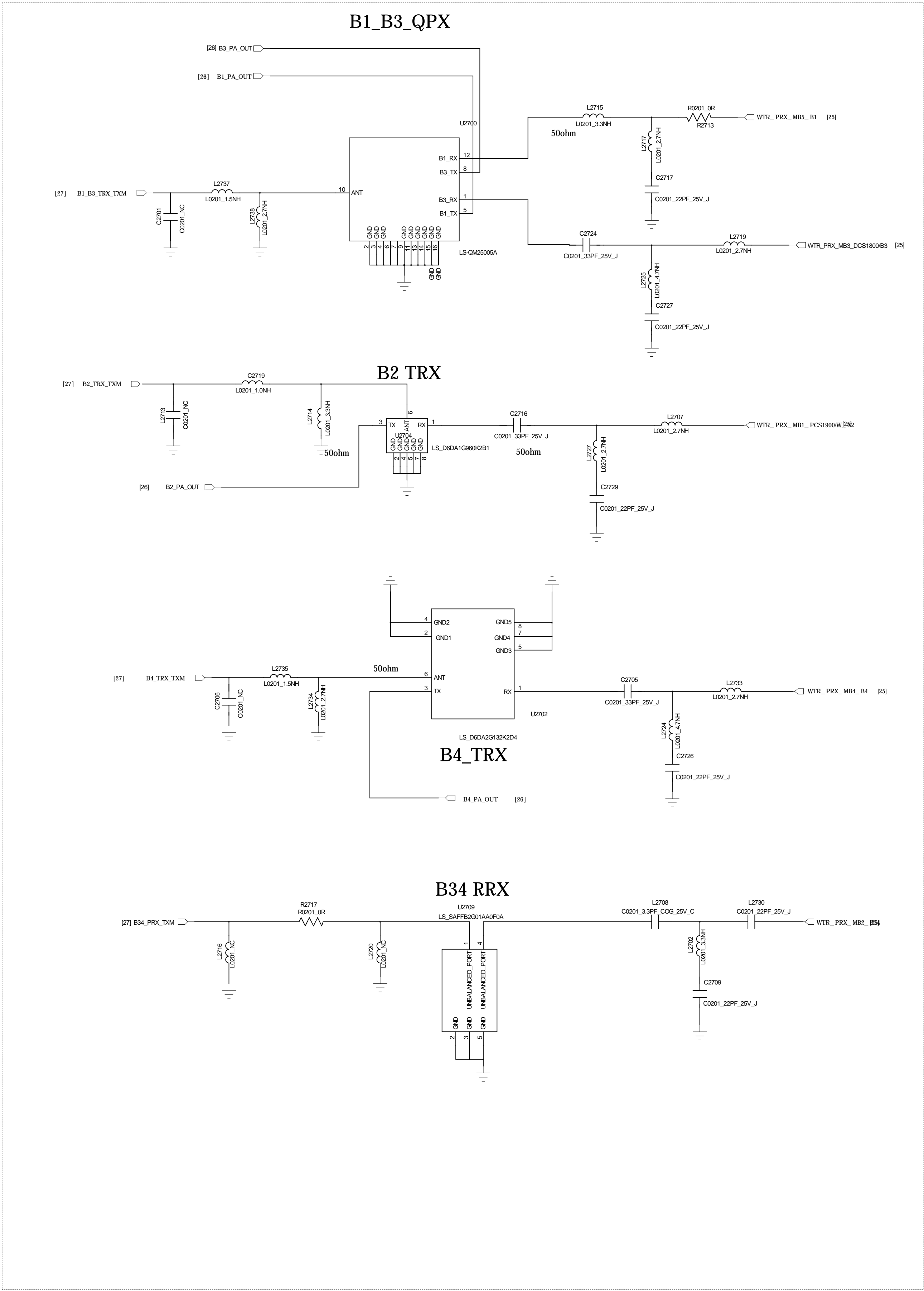
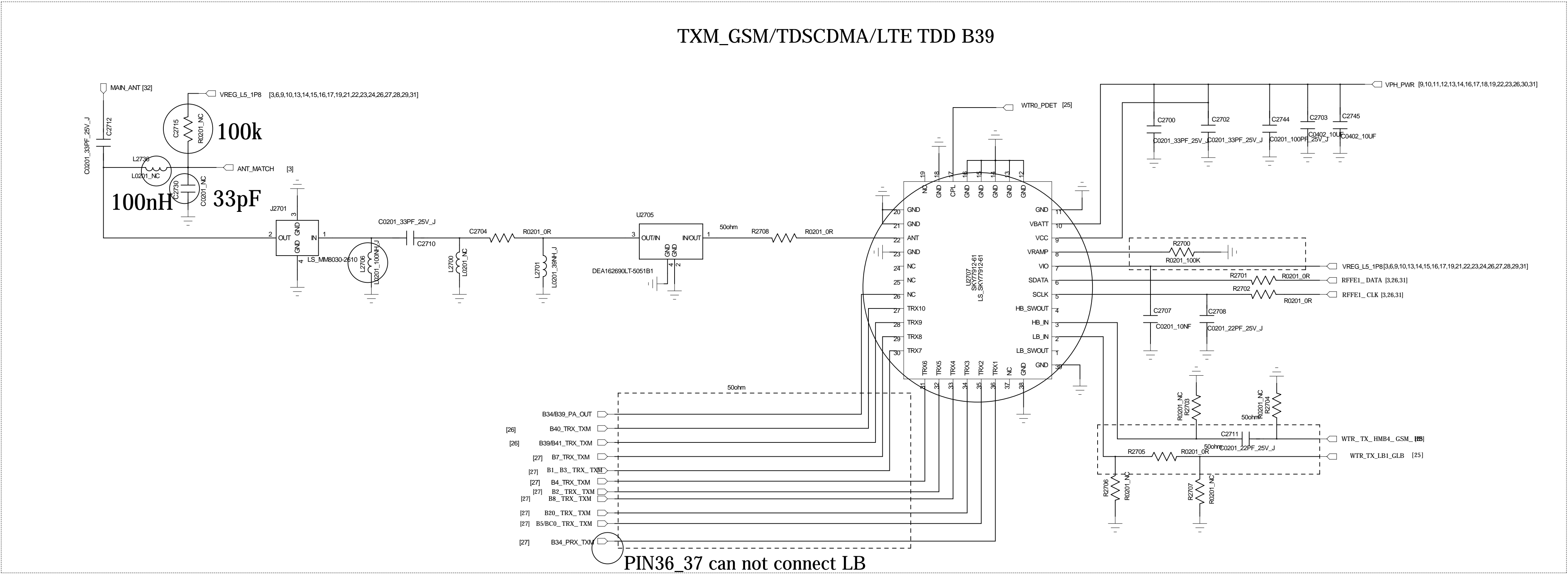
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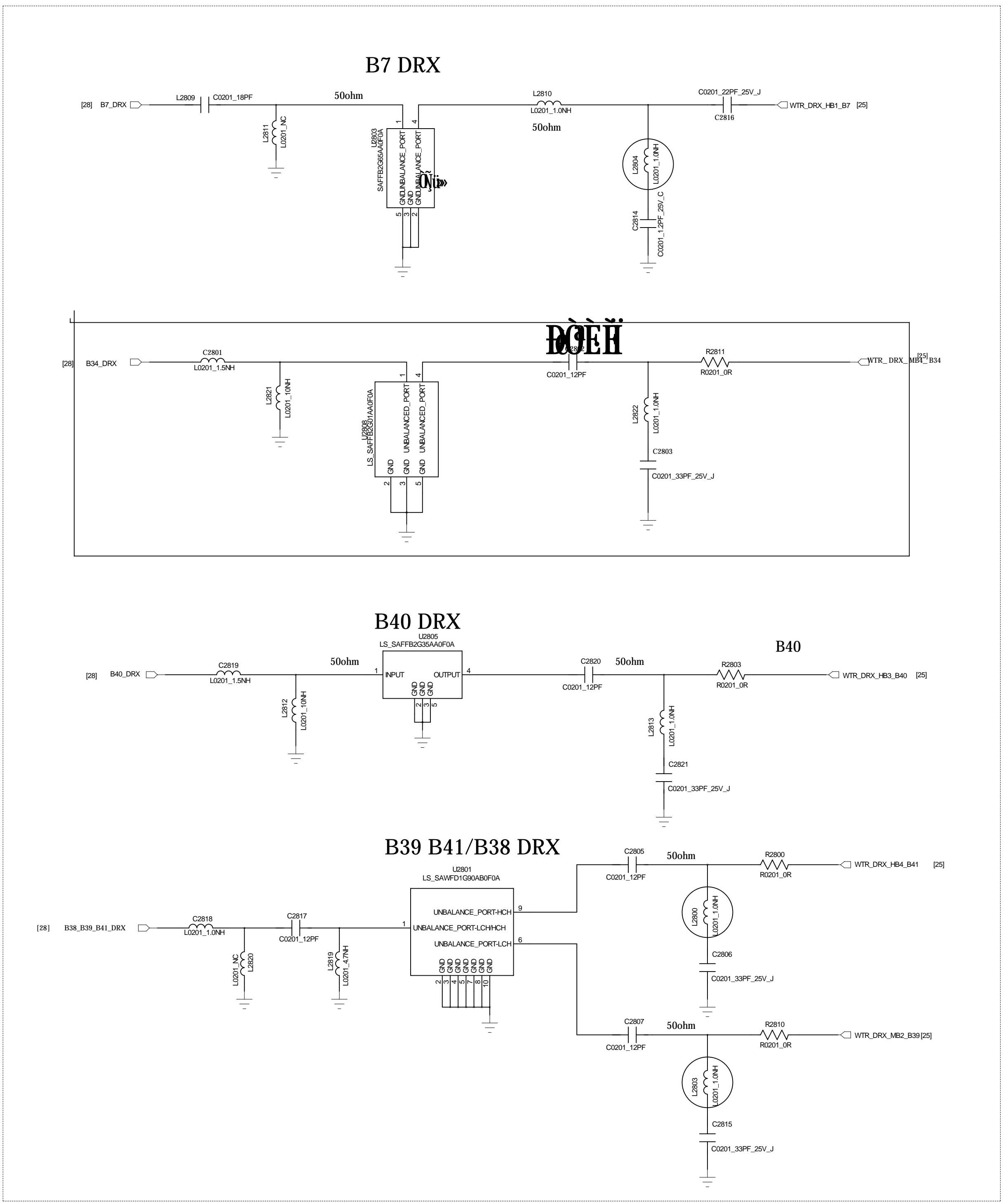
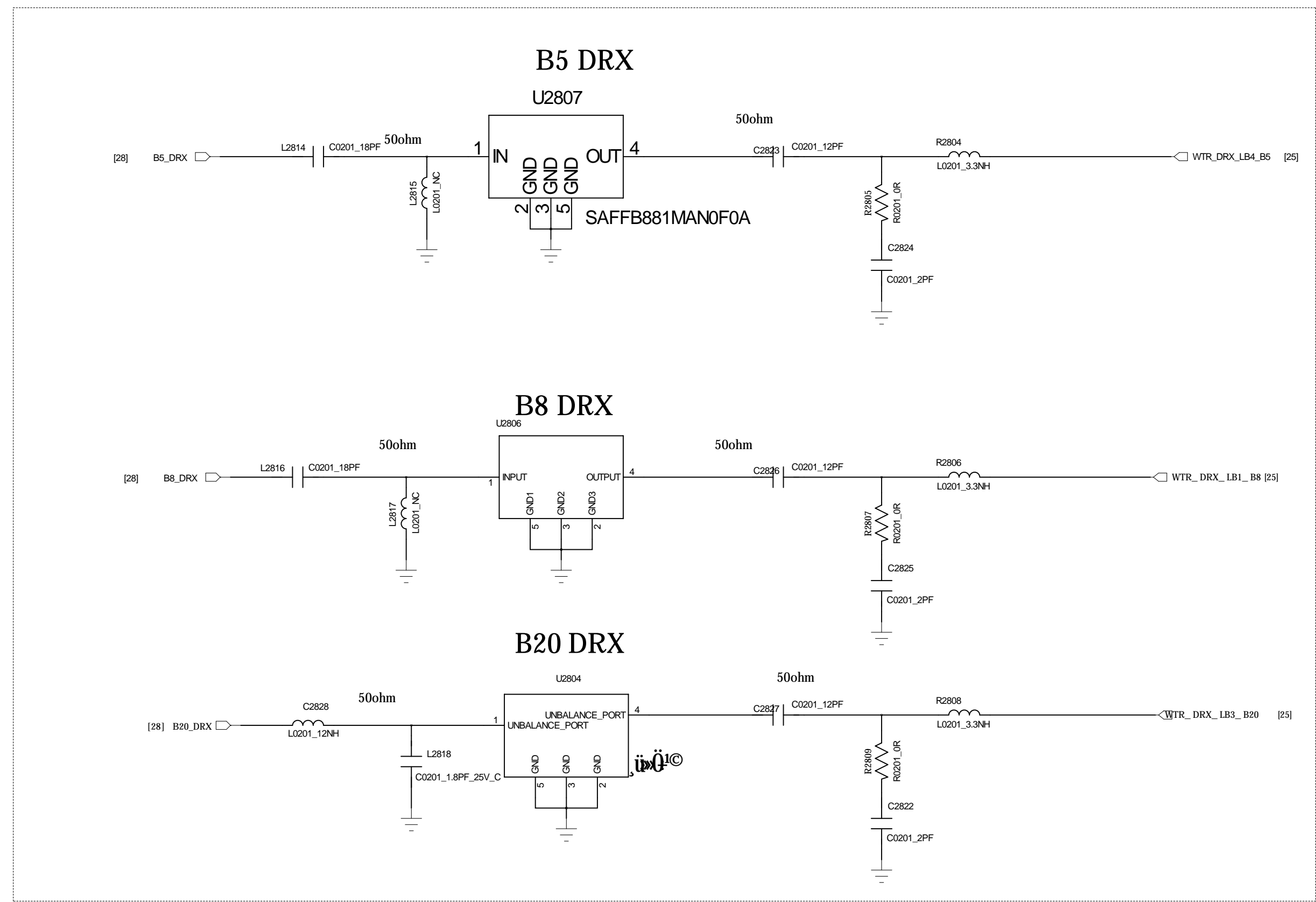
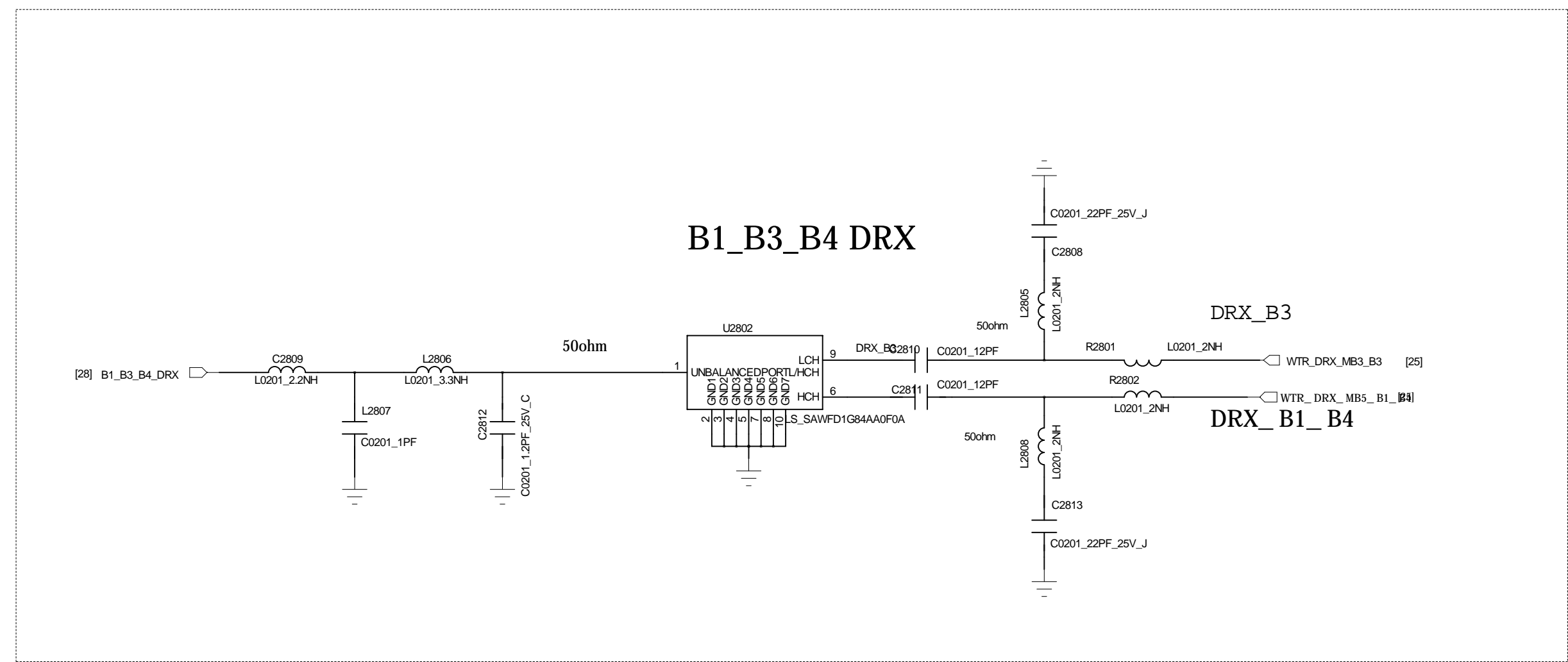
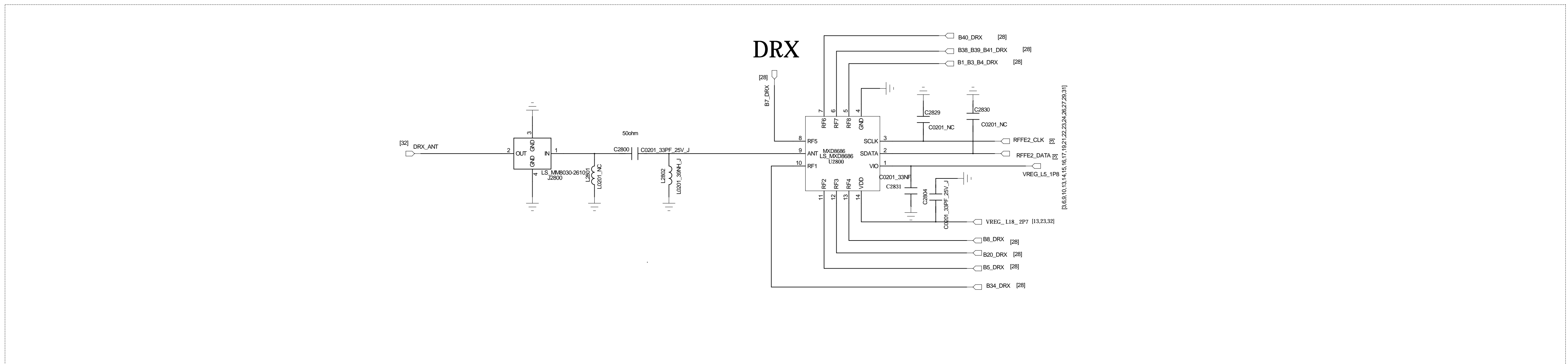


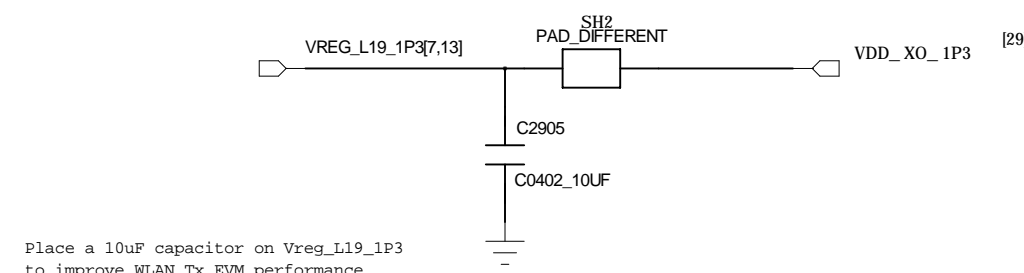
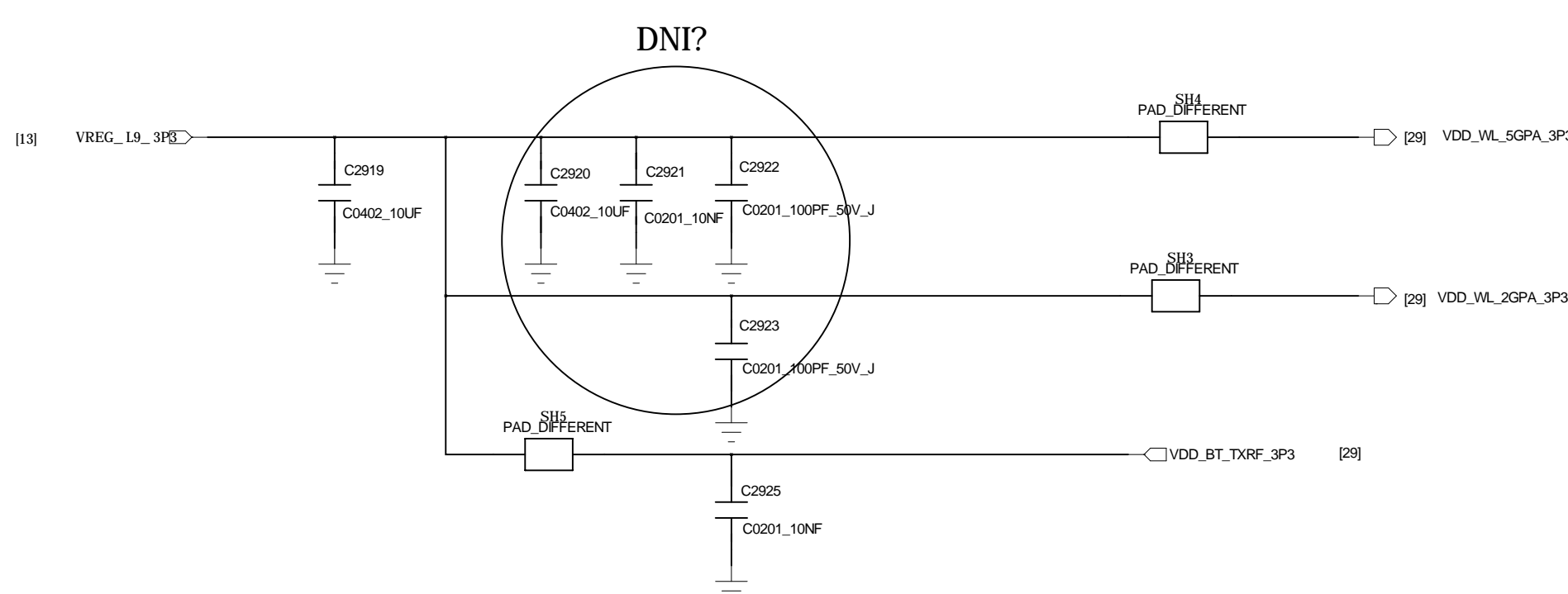
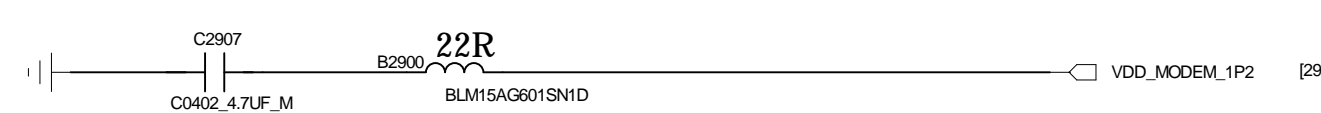
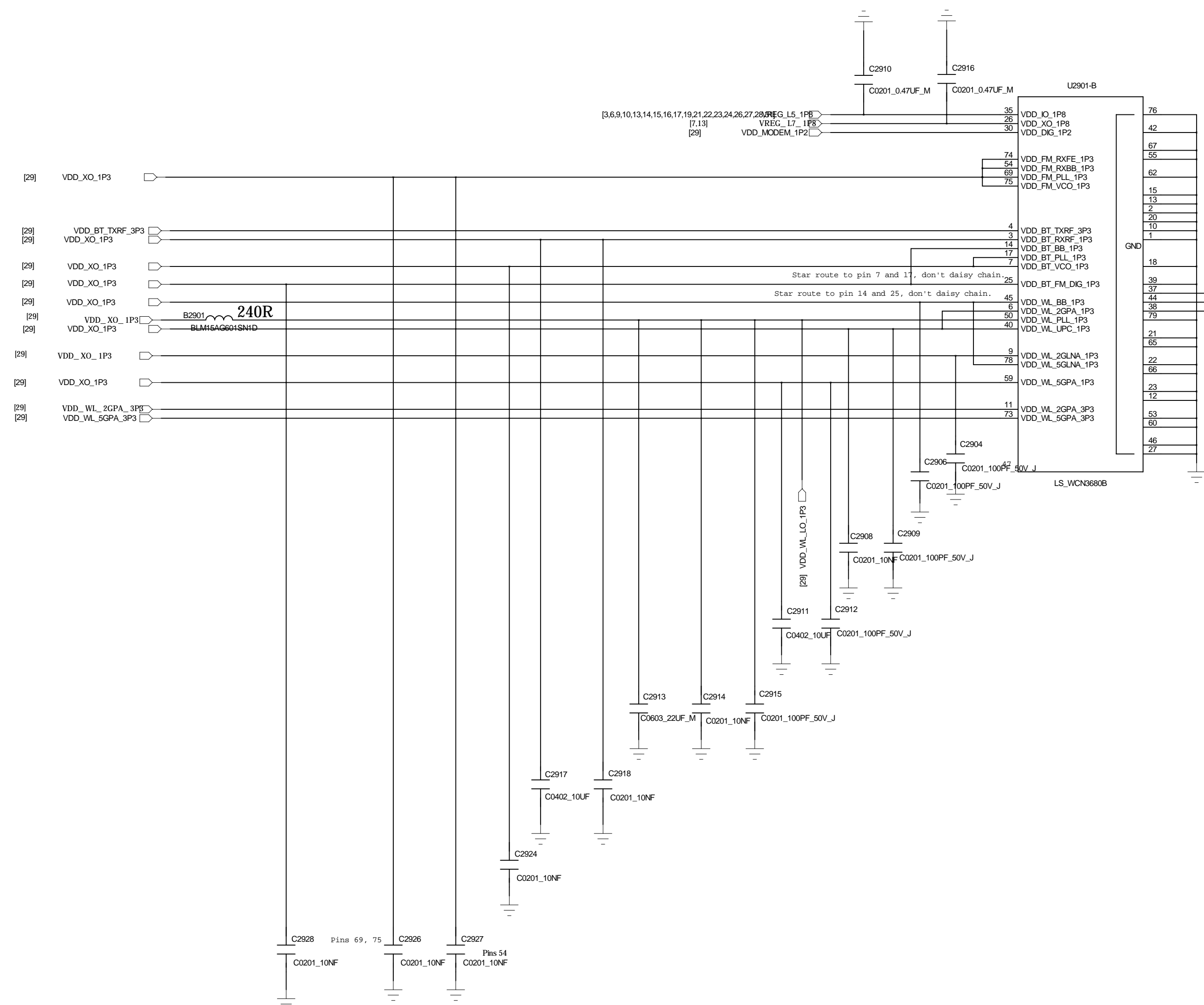
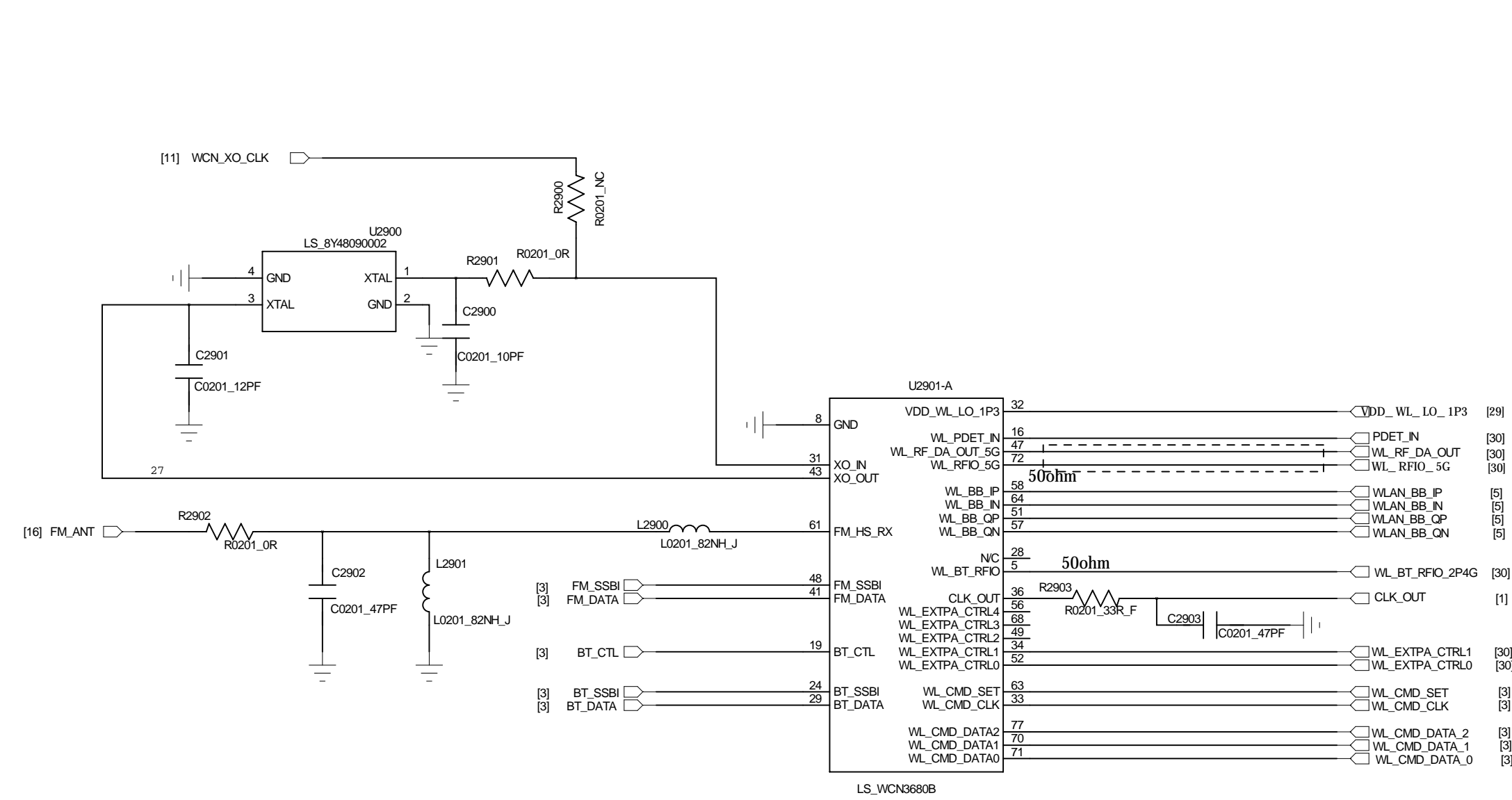
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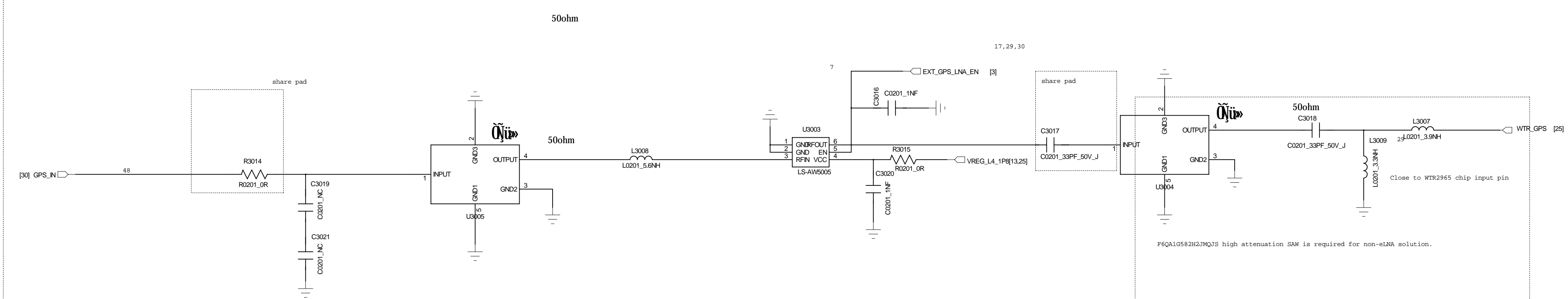
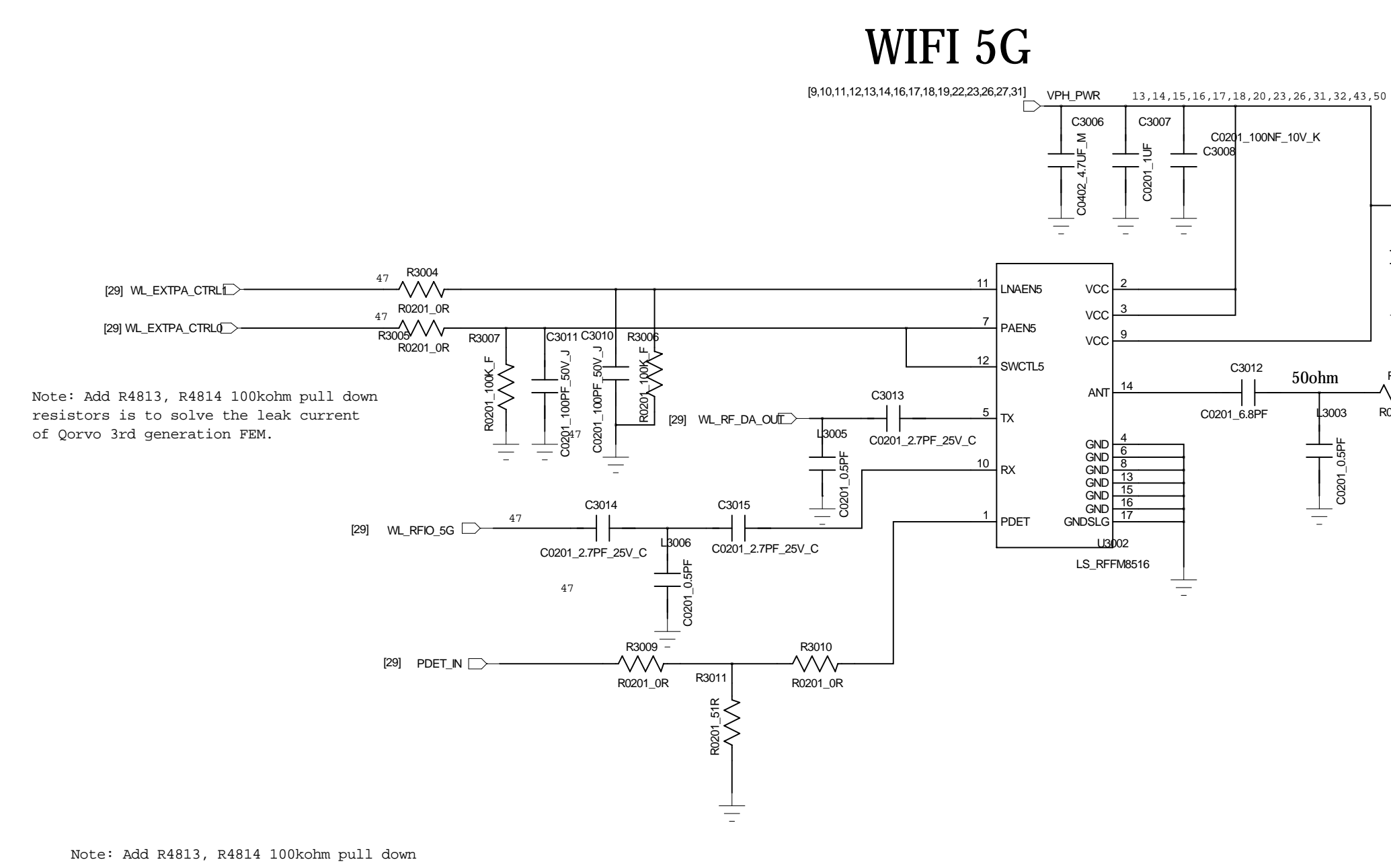
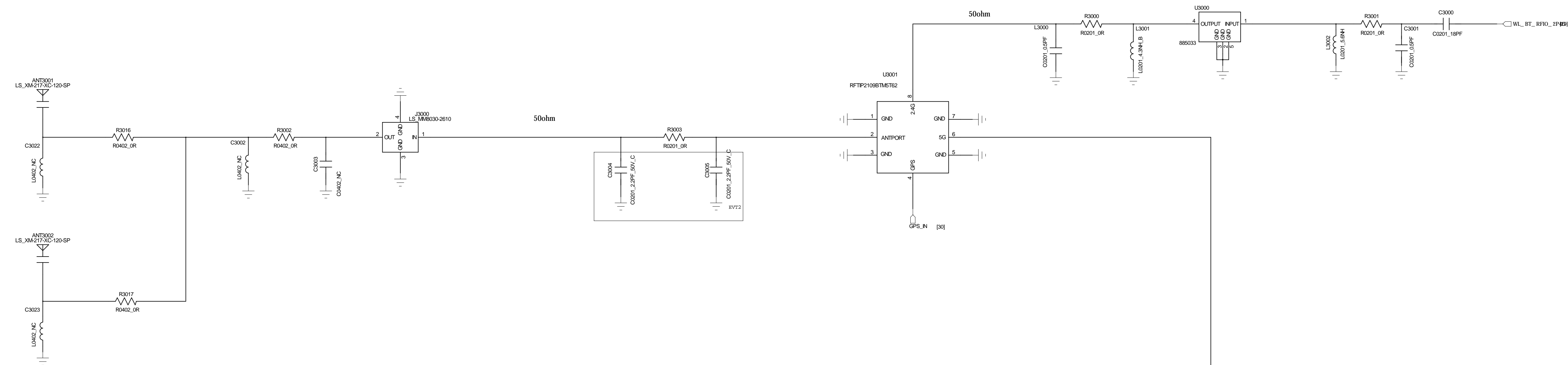
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R4904	0ohm	DNI
R4903	DNI	0ohm
C4903	33pF	DNI
FL4902	SAFFR1G56KB0F0A	F6Q4IG582H2JMQJS

